

PCS CDMA/TDMA/GSM1800 3V PA DRIVER AMPLIFIER

RoHS Compliant & Pb-Free Product

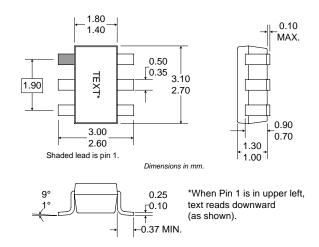
Typical Applications

- TDMA/CDMA/FM PCS Tx Amplifier
- Low Noise Transmit Driver Amplifier
- 2.4GHz WLAN Systems

- GSM1800 Driver Amplifier
- General Purpose Amplification
- Commercial and Consumer Systems

Product Description

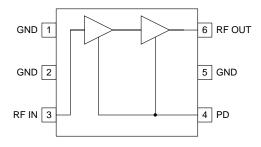
The RF2367 is a low noise CDMA/TDMA/GSM PA driver amplifier with a very high dynamic range designed for transmit digital PCS applications with frequency ranges between 1700MHz and 2000MHz. The device functions as an outstanding PA driver amplifier in the transmit chain of digital subscriber units where low transmit noise power is a concern. The IC includes a power down feature that can be used to completely turn off the device. The RF2367 is featured in a standard SOT23-6 plastic package.



Optimum Technology Matching® Applied

- ☐ Si BJT
 ☐ Si Bi-CMOS
- GaAs HBT
- ☐ GaAs MESFET ☐ Si CMOS

- InGaP/HBT
- ☐ GaN HEMT
- ☐ SiGe Bi-CMOS



Functional Block Diagram

Features

· Low Noise and High Intercept Point

Package Style: SOT23-6

- Adjustable Bias Current
- Power Down Control
- Single 2.5 V to 6.0 V Power Supply
- 150MHz to 2500MHz Operation
- Extremely Small SOT23-6 Package

Ordering Information

RF2367 PCS CDMA/TDMA/GSM1800 3V PA Driver Ampli-

fier

RF2367PCBA-41X Fully Assembled Evaluation Board

 RF Micro Devices, Inc.
 Tel (336) 664 1233

 7628 Thorndike Road
 Fax (336) 664 0454

 Greensboro, NC 27409, USA
 http://www.rfmd.com

RF2367

Absolute Maximum Ratings

| Parameter | Rating | Unit | | | | |
|---------------------|--------------|----------|--|--|--|--|
| Supply Voltage | -0.5 to +8.0 | V_{DC} | | | | |
| Input RF Level | +10 | dBm | | | | |
| Storage Temperature | -40 to +150 | °C | | | | |



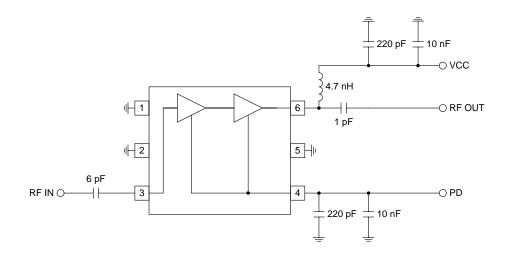
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| Parameter | Specification | | Unit | Condition | | |
|---------------------------------------|---------------|-------------|------|-----------|---|--|
| Farailletei | Min. | Тур. | Max. | Unit | Condition | |
| Operating Range | | | | | | |
| Overall Frequency Range | | 150 to 2500 | | MHz | | |
| Supply Voltage (V _{CC}) | 2.5 | | 6.0 | V | | |
| Power Down Voltage (V _{PD}) | 2.7 | | 2.9 | V | For normal operation | |
| | | | 0.9 | V | For power down operation | |
| Total Current Consumption | 24 | 37 | 45 | mA | V _{CC} =3.0V, V _{PD} =2.8V | |
| | | | 10 | μΑ | V _{CC} =3.0V, V _{PD} <0.9V | |
| Operating Ambient Temperature | -40 | | +85 | °C | | |
| Input Impedance | | 50 | | Ω | | |
| Output Impedance | | 50 | | Ω | | |
| 1880MHz Performance | | | | | All parameters measured from evaluation board with T=25°C, RF=1880MHz, V _{CC} =3.0V, V _{PD} =2.8V | |
| Gain | 20 | 21.5 | 23 | dB | | |
| Output IP3 | +20 | +24 | | dBm | | |
| Noise Figure | | 2.2 | 2.5 | dB | | |
| Reverse Isolation | 32 | 34 | | dB | | |
| Input VSWR | | 1.9:1 | 2:1 | | | |
| Output VSWR | | 1.5:1 | 2:1 | | Using External LC network used on Evaluation Board | |
| Output P _{1dB} | +13 | +14 | +15 | dBm | | |

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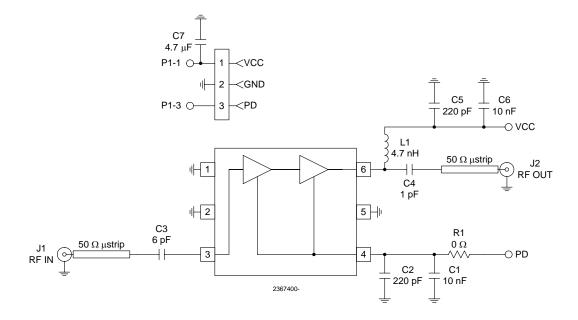
| Pin | Function | Description | Interface Schematic |
|-----|----------|--|---------------------|
| 1 | GND | Ground connection. Keep traces physically short and connect immediately to ground plane for best performance. | |
| 2 | GND | Same as pin 1. | |
| 3 | RF IN | RF input pin. This pin is DC coupled and internally matched to a <2:1 VSWR at 1880MHz. | PD TO OUTPUT STAGE |
| 4 | PD | Power Down for the IC. V_{PD} = 2.8V +/- 0.1V turns on the Part. V_{PD} <0.9V turns off the Part. Lower threshold for device operation is approximately 1.2V. External RF bypassing is required. The trace length between the pin and the bypass capacitors should be minimized. The ground side of the bypass capacitors should connect immediately to ground plane. Nominal current for this pin for V_{PD} = 2.8V is 8mA typical. | See pin 3. |
| 5 | GND | Same as pin 1. | |
| 6 | RF OUT | Amplifier Output pin. This pin is an open-collector output. It must be biased to either V_{CC} or pin 4 through a choke or matching inductor. This pin is typically externally matched to 50Ω with a shunt bias/matching inductor and series blocking/matching capacitor. Refer to application/evaluation board schematics. | |

Application Schematic: ~1880 MHz Operation



Evaluation Board Schematic

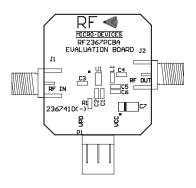
(Download Bill of Materials from www.rfmd.com.)

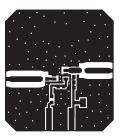


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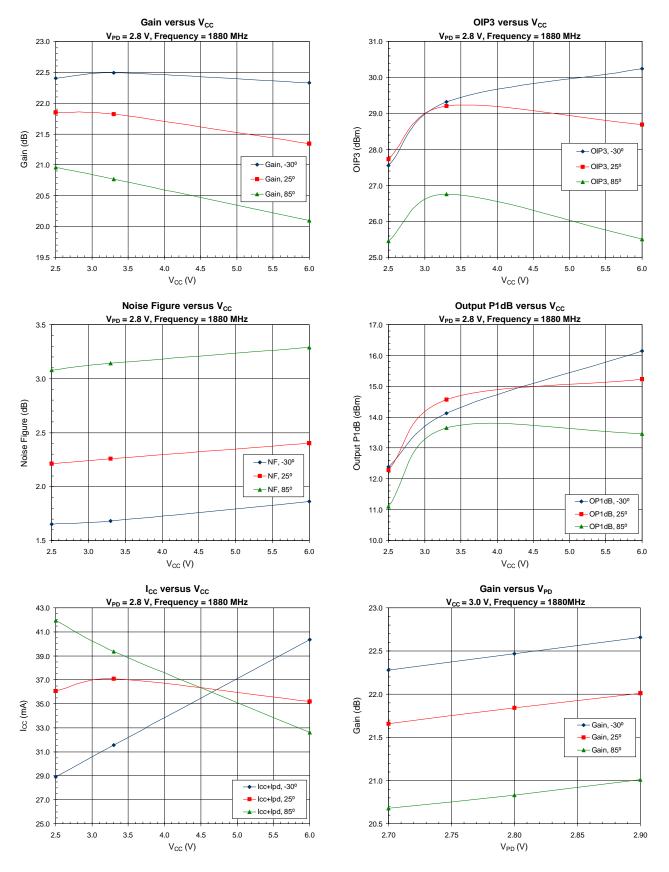
Evaluation Board Layout Board Size 0.948" x 1.063"

Board Thickness 0.031", Board Material FR-4

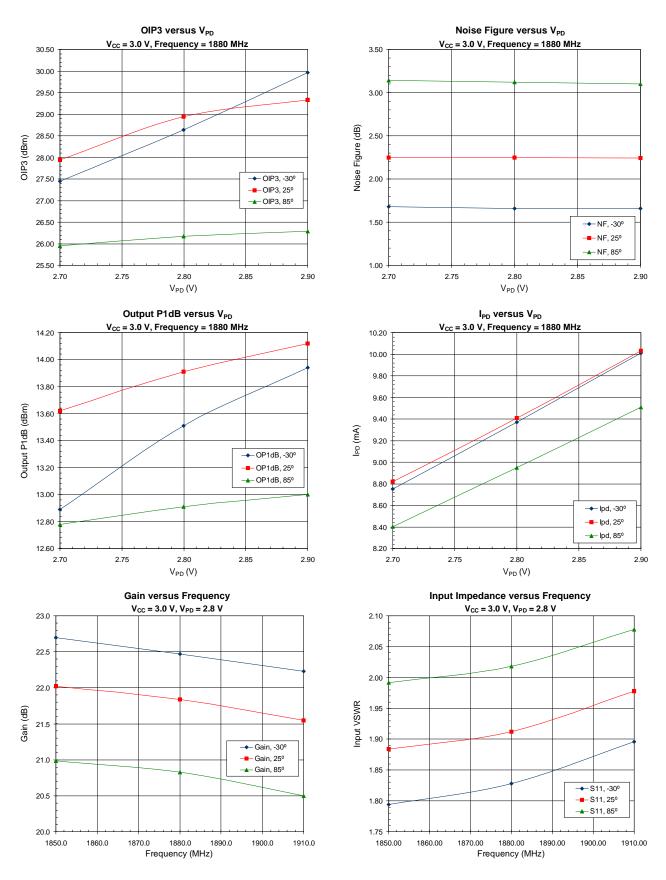




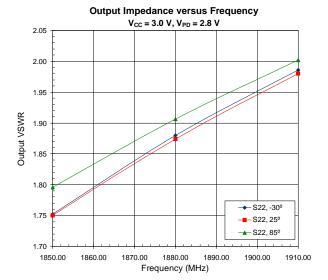




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