



Crystal Clock Oscillator

T-50-23

HS-150 Series (TTL)
HS-360 Series (CMOS)
63.0 MHz - 200.0 MHz



Description

The **HS-150 Series** of quartz crystal clock oscillators are general purpose, TTL and STTL compatible oscillators. The **HS-360 Series** are general purpose CMOS oscillators (including 4000 Series CMOS, AC MOS, MOS, HCMOS, 74C, 74HC and NMOS).

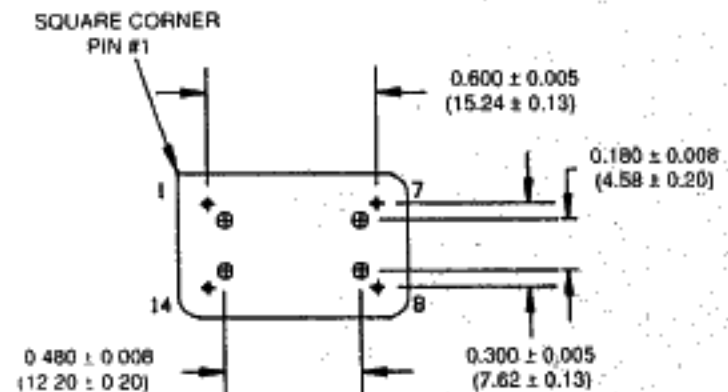
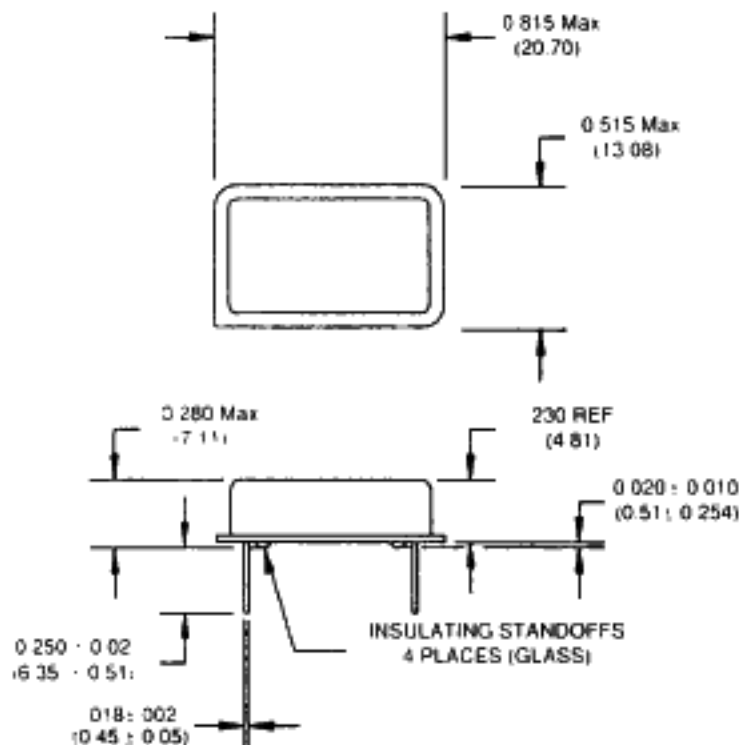
All units are resistance welded in an all metal package, offering RFI shielding, and are designed to survive standard wave soldering operations without damage. Insulated standoffs to enhance board cleaning are standard.

Suggested Applications

- HS-150 and HS-360 Series oscillators are ideally suited for microprocessor timing control and use in instrument controllers. The HS-150 Series is TTL compatible; the HS-360 Series is CMOS compatible.

Features

- Wide frequency range - 63.0 to 200.0 MHz (consult factory for higher frequency requirements)
- User specified tolerance from $\pm 0.0025\%$
- Low power consumption
- All metal, resistance weld, hermetically sealed package
- High shock resistance, to 3000 G's



Pin	Connection
1	N.C.
7	grd and case
8	Output
14	V _{CC} -V _{DD}

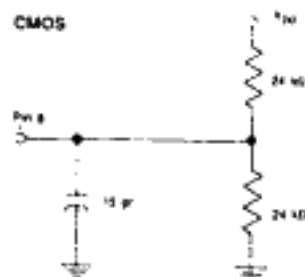
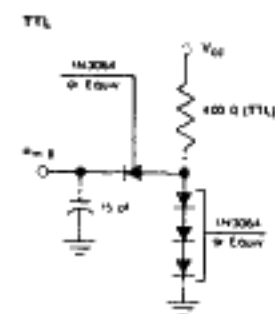
Dimensions are for reference only, Inches (mm).

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Parameter	Min	Max	Units
V _C DC Supply Voltage	-0.5	7.0*	V
T _S Storage Temperature	-65	125	°C
P _D Power Dissipation		450	mW
T _L Lead Temperature (soldering, 10 sec)		300	°C

Equivalent Loads



Optional 50pF load for HCMOS & AC MOS also available

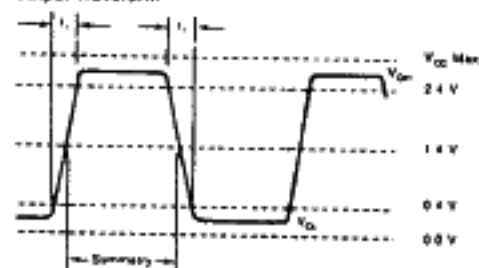
PARAMETER	CONDITIONS	MINIMUM	MAXIMUM
General Characteristics			
Supply voltage (V _{CC} , V _{DD})	Supply Breakdown	4.75 V ¹ -0.5 V	5.25 V 7.0 V
Supply current (I _{CC} , I _{DD})	No Output Load, @ 100 MHz	0.0 mA	80 mA
Output current (I _O)	—	0.0 mA	16.0 mA
Operating temperature (T _A) ²	—	0°C	70°C
Storage temperature (T _S)	—	-65°C	+125°C
Power dissipation (P _D)	—	—	450 mW
Lead temperature (T _L)	Soldering, 10 s	—	300°C
Output Characteristics			
Frequency	—	63.0	200.0 MHz
Tolerance	User Specified	±0.0025%	—
Symmetry	TTL, @ 1.4 V CMOS, @ 0.5 V _{DD}	40/60%	60/40% 60/40%
Logic 0 (V _{OL})	TTL, driving equiv. load CMOS, driving equiv. load	—	0.4 V 0.2 V
Logic 1 (V _{OH})	TTL driving equiv. load CMOS, driving equiv. load	2.4 V V _{DD} - 0.2 V	—
Logic 0 (I _{OL} sink)	TTL driving equiv. load CMOS, driving equiv. load	—	16 mA 600 μA
Logic 1 (I _{OH} source)	TTL driving equiv. load CMOS, driving equiv. load	—	400 μA 600 μA
Rise & fall time (t _r , t _f)	TTL, @ 0.4 V to 2.4 V CMOS, @ 10% to 90% of V _{DD}	—	3 ns 3 ns

¹ Lower voltage operation option to 3V available.

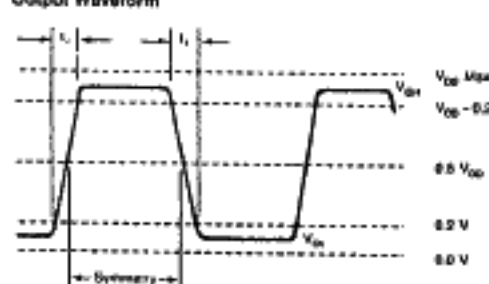
² -55°C to +125°C available for some frequencies.

* Overvoltage causes the oscillator to draw extreme current, and damage occurs.

TTL Output Waveform



CMOS Output Waveform



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