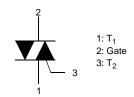


FKN2L80

Application Explanation

- Switching mode power supply, light dimmer, electric flasher unit, hair drier
- TV sets, stereo, refrigerator, washing machine
- Electric blanket, solenoid driver, small motor control
- Photo copier, electric tool





Bi-Directional Triode Thyristor Planar Silicon

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units	
V_{DRM}	Repetitive Peak Off-State Voltage (Note1)	800	V	

Symbol	Parameter	Conditions		Rating	Units
I _{T (RMS)}	RMS On-State Current	Commercial frequency, sine full wave 360° conduction, Tc=74 $^{\circ}\mathrm{C}$		1.5	Α
I _{TSM}	Surge On-State Current	Sinewave 1 full cycle, peak value,	50Hz	9	Α
		non-repetitive 6		10	Α
l ² t	I ² t for Fusing	Value corresponding to 1 cycle of halfwave, surge on-state current, tp=10ms		0.4	A ² s
di/dt	Critical Rate of Rise of On-State Current	$I_G = 2x I_{GT}, tr \le 100ns$		50	A/μs
P _{GM}	Peak Gate Power Dissipation			1	W
P _{G (AV)}	Average Gate Power Dissipation			0.1	W
V_{GM}	Peak Gate Voltage			6	V
I _{GM}	Peak Gate Current			0.5	Α
T _J	Junction Temperature			- 40 ~ 125	°C
T _{STG}	Storage Temperature			- 40 ~ 125	°C

Thermal Characteristic

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
R _{th(J-C)}	Thermal Resistance	Junction to case (Note 4)	-	-	40	°C/W

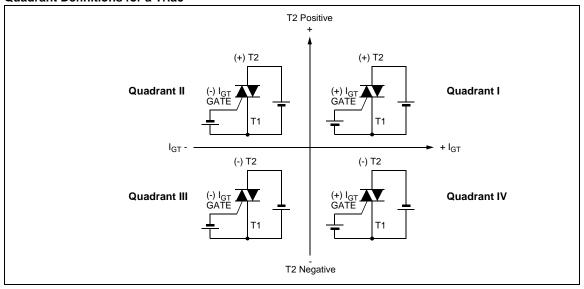
$\textbf{Electrical Characteristics} \ \, \textbf{T}_{\text{C}} = 25^{\circ} \text{C unless otherwise noted}$

Symbol	Parameter		Test Condition		Min.	Тур.	Max.	Units
I _{DRM}	Repetieive Peak Off-State Current		V _{DRM} applied		-	-	20	μΑ
V _{TM}	On-State Voltage		T _C =25°C, I _{TM} =3A Instantaneous measurement		-	-	1.6	V
		I	V _D =12V, R _L =20Ω	T2(+), Gate (+)	-	-	1.5	V
V_{GT}	Gate Trigger Voltage (Note 2)	II		T2(+), Gate (-)	-	-	1.5	V
		III		T2(-), Gate (-)	-	-	1.5	V
		I		T2(+), Gate (+)	-	-	10	mA
I_{GT}	Gate Trigger Current (Note 2)	II	$V_D=12V$, $R_L=20\Omega$	T2(+), Gate (-)	-	-	10	mA
		III		T2(-), Gate (-)	-	-	10	mA
V _{GD}	Gate Non-Trigger Voltage		T _J =125°C, V _D =1/2V _{DRM}		0.2	-	-	V
I _H	Holding Current		$V_D = 12V, I_{TM} = 1A$				5	mA
IL	Latching Current	I, III	, III $V_D = 12V$, $I_G = 1.2I_{GT}$				5	mA
		II					5	mA
dv/dt	Critical Rate of Rise of Off-State Voltag		V_{DRM} = Rated, T_j = 125°C, Exponential Rise			500		V/µs
(dv/dt) _C	Critical-Rate of Rise of Off-State Commutating Voltage (Note 3)				5	-	-	V/µs

- Notes:
 1. Gate Open
 2. Measurement using the gate trigger characteristics measurement circuit
 3. The critical-rate of rise of the off-state commutating voltage is shown in the table below
 4. Case temperature is measured at the T2 terminal 1.5mm away from the molded case.

V _{DRM} (V)	Test Condition	Commutating voltage and current waveforms (inductive load)
FKN2L80	1. Junction Temperature T _J =125°C 2. Rate of decay of on-state commutating current (di/dt) _C = - 0.5A/ms 3. Peak off-state voltage V _D = 400V	Supply Voltage Time Main Current Main Voltage (dv/dt) _C Time V _D

Quadrant Definitions for a Triac



Typical Curves

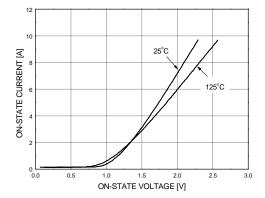


Figure 1. Maximum On-state Characteristics

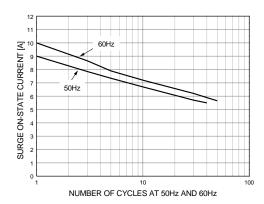


Figure 2. Rated Surge On-state Current

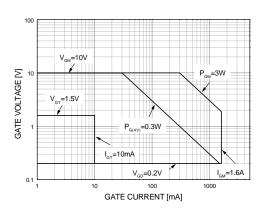


Figure 3. Gate Characteristics

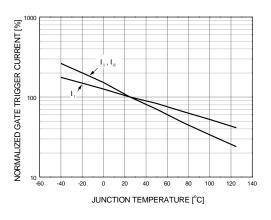


Figure 4. Gate Trigger Current vs Tj

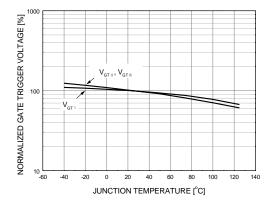


Figure 5. Gate Trigger Voltage vs Tj

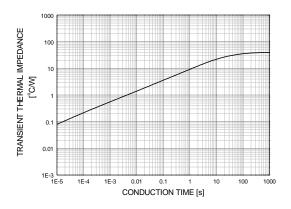


Figure 6. Transient Thermal Impedance

Typical Curves (Continues)

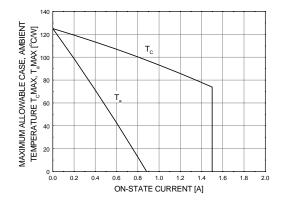


Figure 7. Allowable Case, Ambient Temperature vs Rms On-state Current

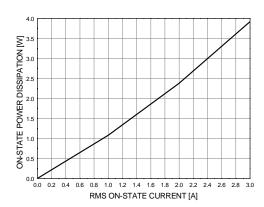


Figure 8. Maximum On-state Power Dissipation

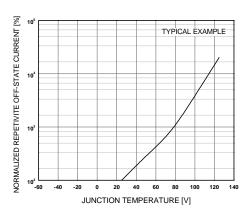


Figure 9. Repetitive Peak Off-state Current vs Junction Temperature

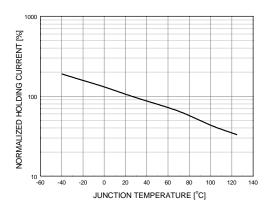


Figure 10. Holding Current vs
Junction Temperature

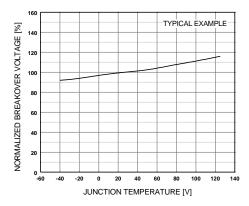


Figure 11. Breakover Voltage vs Junction Temperature

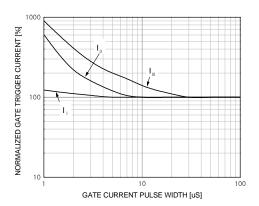


Figure 12. Gate Trigger Current vs
Gate Current Pulse Width

Typical Curves (Continues)

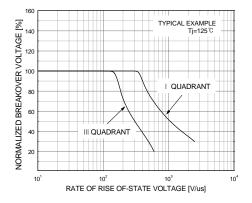
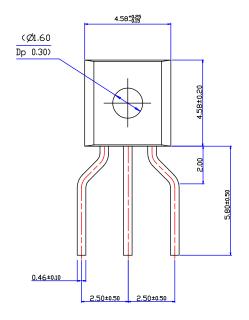
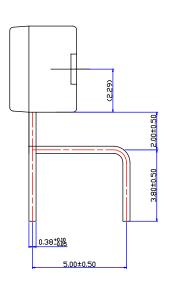


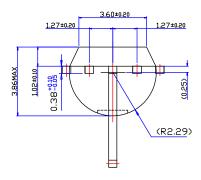
Figure 13. Breakover Voltage vs Rate of Rise of Off-state Voltage

Package Dimension

TO-92







Dimensions in Millimeters

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Rev. I5

PRODUCT STATUS DEFINITIONS

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