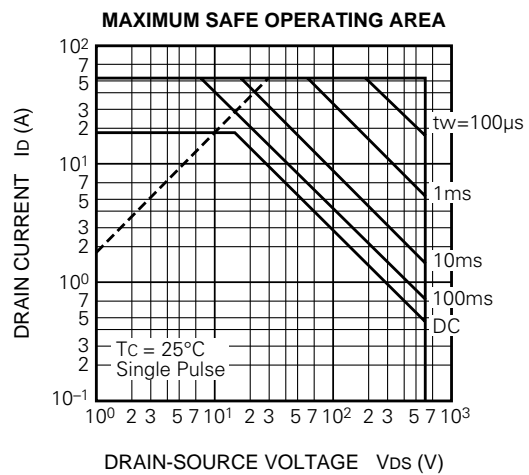
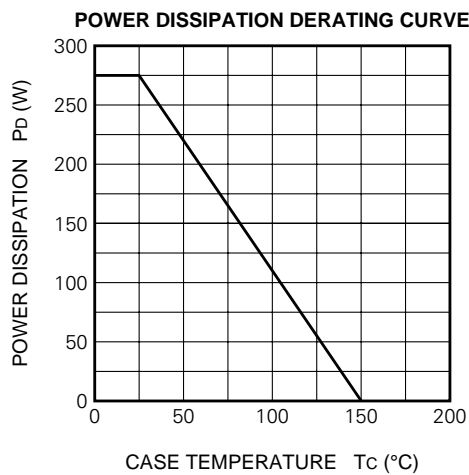




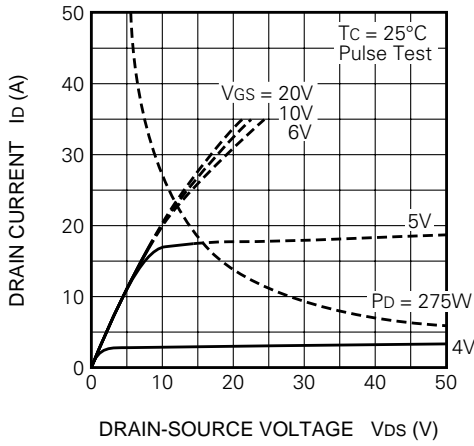
**ELECTRICAL CHARACTERISTICS** (T<sub>ch</sub> = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	I <sub>D</sub> = 1mA, V <sub>GS</sub> = 0V	600	—	—	V
V (BR) GSS	Gate-source breakdown voltage	I <sub>G</sub> = ±100μA, V <sub>DS</sub> = 0V	±30	—	—	V
I <sub>GSS</sub>	Gate-source leakage current	V <sub>GS</sub> = ±25V, V <sub>DS</sub> = 0V	—	—	±10	μA
I <sub>DSS</sub>	Drain-source leakage current	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	—	—	1	mA
V <sub>GS</sub> (th)	Gate-source threshold voltage	I <sub>D</sub> = 1mA, V <sub>DS</sub> = 10V	2	3	4	V
r <sub>DS</sub> (ON)	Drain-source on-state resistance	I <sub>D</sub> = 9A, V <sub>GS</sub> = 10V	—	0.42	0.54	Ω
V <sub>DS</sub> (ON)	Drain-source on-state voltage	I <sub>D</sub> = 9A, V <sub>GS</sub> = 10V	—	3.78	4.86	V
y <sub>fs</sub>	Forward transfer admittance	I <sub>D</sub> = 9A, V <sub>DS</sub> = 10V	8.0	13	—	S
C <sub>iss</sub>	Input capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz	—	2800	—	pF
C <sub>oss</sub>	Output capacitance		—	350	—	pF
C <sub>rss</sub>	Reverse transfer capacitance		—	50	—	pF
t <sub>d</sub> (on)	Turn-on delay time		—	50	—	ns
t <sub>r</sub>	Rise time	V <sub>DD</sub> = 200V, I <sub>D</sub> = 9A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = R <sub>GS</sub> = 50Ω	—	85	—	ns
t <sub>d</sub> (off)	Turn-off delay time		—	350	—	ns
t <sub>f</sub>	Fall time		—	100	—	ns
V <sub>SD</sub>	Source-drain voltage	I <sub>S</sub> = 9A, V <sub>GS</sub> = 0V	—	1.5	2.0	V
R <sub>th</sub> (ch-c)	Thermal resistance	Channel to case	—	—	0.45	°C/W
t <sub>rr</sub>	Reverse recovery time	I <sub>S</sub> = 18A, di <sub>S</sub> /dt = -100A/μs	—	—	150	ns

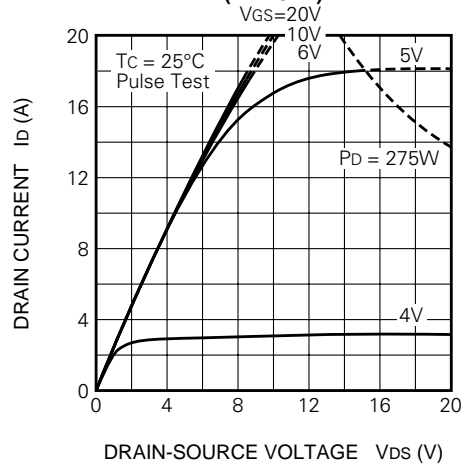
**PERFORMANCE CURVES**



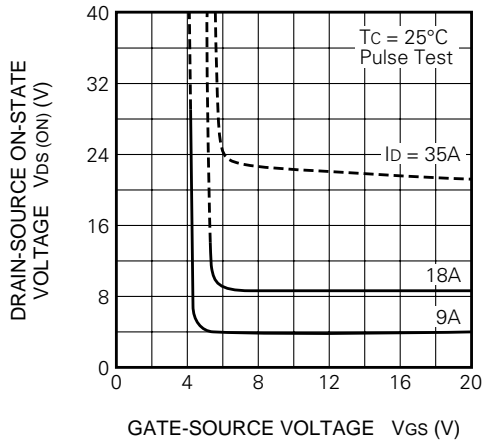
OUTPUT CHARACTERISTICS (TYPICAL)



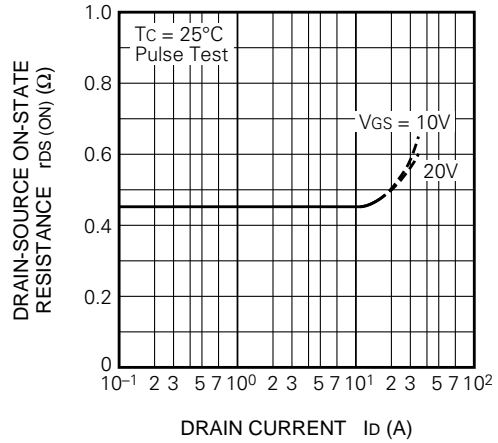
OUTPUT CHARACTERISTICS (TYPICAL)



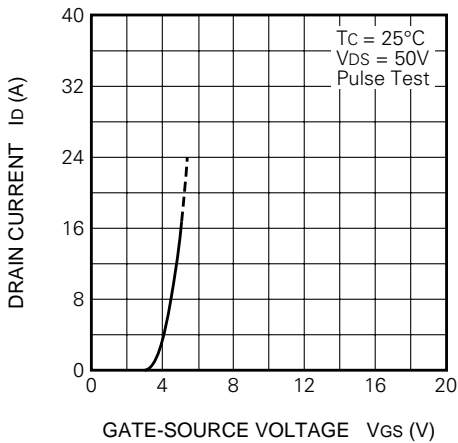
ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)



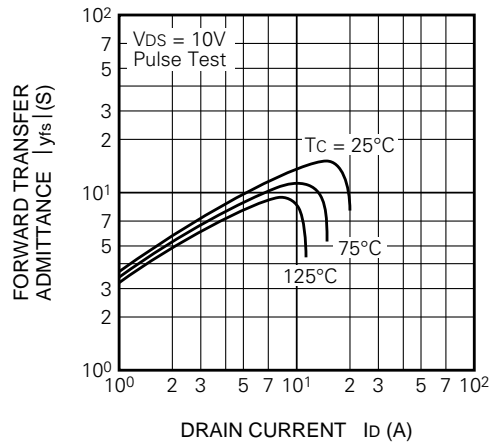
ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)



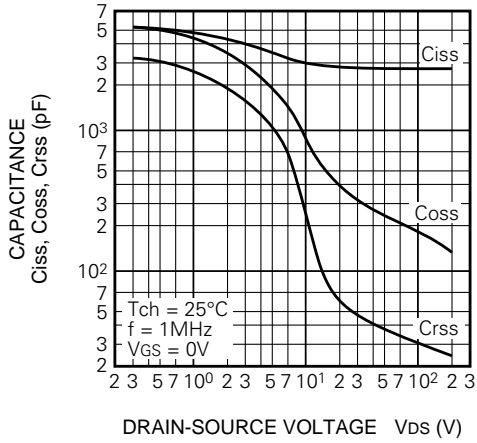
TRANSFER CHARACTERISTICS (TYPICAL)



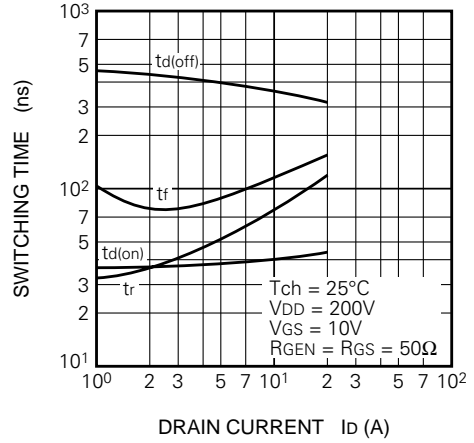
FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)



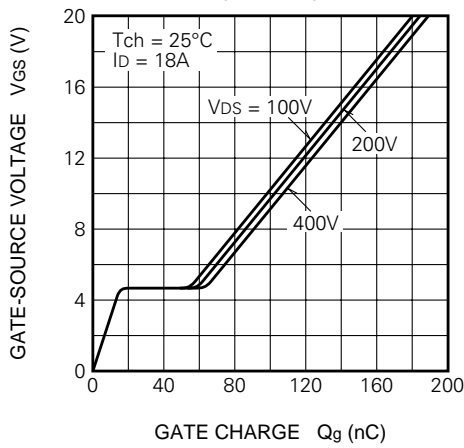
**CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)**



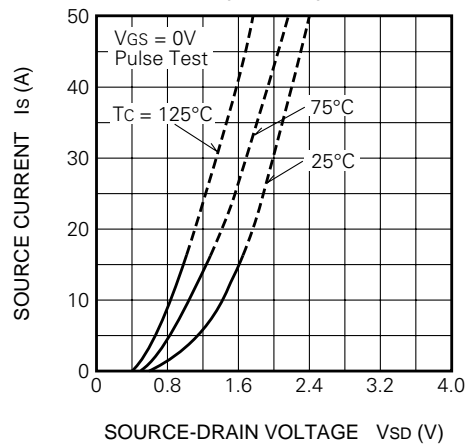
**SWITCHING CHARACTERISTICS (TYPICAL)**



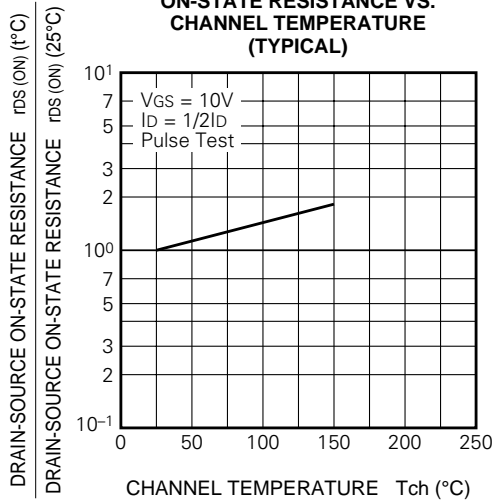
**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**



**ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)**



**THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**

