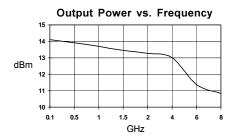


### **Product Description**

Sirenza Microdevices' SNA-186 is a GaAs monolithic broadband amplifier (MMIC) housed in a low-cost surface-mountable plastic package. At 1950 MHz, this amplifier provides 12dB of gain and +13dBm of P1dB power when biased at 50mA.

The use of an external resistor allows for bias flexibility and stability. These unconditionally stable amplifiers are designed for use as general purpose 50 ohm gain blocks.

Also available in chip form (SNA-100), its small size (0.33mm x 0.33mm) and gold metallization makes it an ideal choice for use in hybrid circuits.



## **SNA-186**

# DC-8 GHz, Cascadable **GaAs HBT MMIC Amplifier**



### **Product Features**

- Patented, Reliable GaAs HBT Technology
- Cascadable 50 Ohm Gain Block
- 12dB Gain. +13dBm P1dB
- 1.5:1 Input and Output VSWR
- Operates From a Single DC Supply
- Low Cost Surface Mount Plastic Package

### **Applications**

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameter	Units	Frequency	Min.	Тур.	Max.
$G_{P}$	Small Signal Power Gain	dB dB dB	850 MHz 1950 MHz 2400 MHz	11.5	12.5 12.0 11.8	13.8
$G_{F}$	Gain Flatness	dB	0.1-8 GHz		+/- 0.5	
BW3dB	3dB Bandwidth	GHz			10.0	
P <sub>1dB</sub>	Output Power at 1dB Compression	dBm	1950 MHz		13.0	
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm	1950 MHz		26.0	
NF	Noise Figure	dB	1950 MHz		6.0	
VSWR	Input / Output	-	0.1-10 GHz		1.5:1	
ISOL	Reverse Isolation	dB	0.1-10 GHz		16	
$V_{_{\mathrm{D}}}$	Device Operating Voltage	V		3.3	3.8	4.3
I <sub>D</sub>	Device Operating Current	mA		45	50	55
dG/dT	Device Gain Temperature Coefficient	dB/°C			-0.0015	
R <sub>TH</sub> , j-l	Thermal Resistance (junction to lead)	°C/W			350	
Test Conditions: V <sub>S</sub> = 8 V I <sub>D</sub> = 50 mA Typ. OIP <sub>3</sub> Tone Spacing = 1 MHz, Pout per tone = 0 dBm						

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 $= 25^{\circ}C$ 

 $Z_s = \overline{Z_l} = 50 \text{ Ohms}$ 

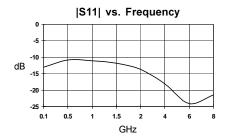
**Test Conditions:** 

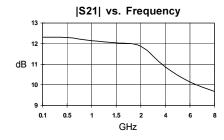
= 82 Ohms

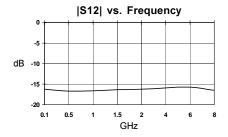


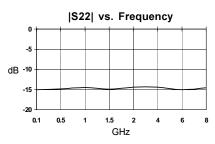
### SNA-186 DC-10 GHz Cascadable MMIC Amplifier

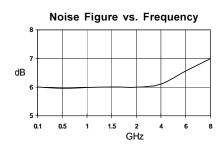
### Typical Performance at 25 $^{\circ}$ C (Vds = 3.8V, Ids = 50mA)

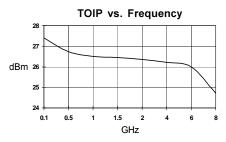












### **Absolute Maximum Ratings**

Parameter	Absolute Limit
Max. Device Current (I <sub>D</sub> )	70 mA
Max. Device Voltage (V <sub>D</sub> )	6 V
Max. RF Input Power	+10 dBm
Max. Junction Temp. (T <sub>J</sub> )	+150°C
Operating Temp. Range (T <sub>L</sub> )	-40°C to +85°C
Max. Storage Temp.	+150°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

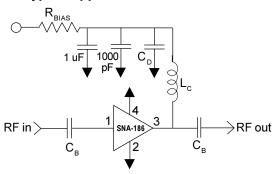
Bias Conditions should also satisfy the following expression:  $I_nV_n < (T_1 - T_1) / R_{T_n}$ , j-I

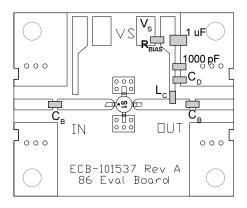
Phone: (800) SMI-MMIC



### SNA-186 DC-10 GHz Cascadable MMIC Amplifier

### **Typical Application Circuit**





#### Part Identification Marking

The part will be marked with an "S1" designator on the top surface of the package.





#### **Application Circuit Element Values**

Reference	Frequency (Mhz)					
Designator	500	500 850 1950 240		2400	3500	
C <sub>B</sub>	220 pF	100 pF	68 pF	56 pF	39 pF	
C <sub>D</sub>	100 pF	68 pF	22 pF	22 pF	15 pF	
L <sub>c</sub>	68 nH	33 nH	22 nH	18 nH	15 nH	

Recommended Bias Resistor Values for I <sub>D</sub> =50mA					
Supply Voltage(V <sub>s</sub> )	6 V	8 V	10 V	12 V	
R <sub>BIAS</sub>	43 Ω	82 Ω	120 Ω	160 Ω	
Note: R <sub>BIAS</sub> provides DC bias stability over temperature.					

#### **Mounting Instructions**

- 1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
- We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description	
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.	
3	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.	

#### **Part Number Ordering Information**

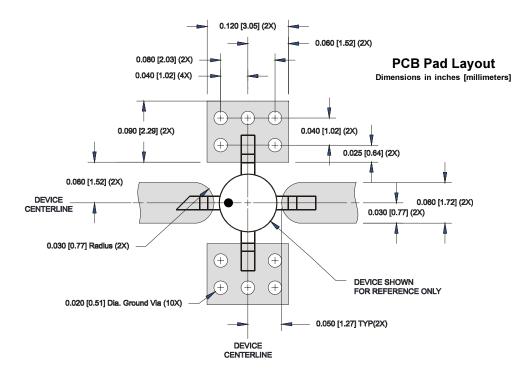
Part Number	Reel Size	Devices/Reel
SNA-186-TR1	7"	1000
SNA-186-TR2	13"	3000
SNA-186-TR3	13"	5000

522 Almanor Ave., Sunnyvale, CA 94085 Phone: (800) SMI-MMIC http://www.sirenza.com

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### SNA-186 DC-10 GHz Cascadable MMIC Amplifier



### **Nominal Package Dimensions**

Dimensions in inches [millimeters]
Refer to drawing posted at www.sirenza.com for tolerances.

