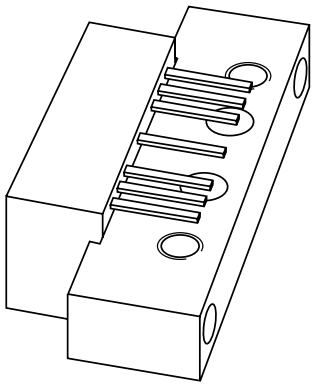
DATA SHEET



BGY68
75 MHz, 30 dB gain reverse amplifier

Product specification
Supersedes data of 1997 Apr 14

2001 Oct 18

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75 MHz, 30 dB gain reverse amplifier BGY68

FEATURES

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability.

APPLICATIONS

Reverse amplifier in two-way CATV systems in the 5 to 75 MHz frequency range.

DESCRIPTION

Hybrid high dynamic range amplifier module in a SOT115J package operating at a voltage supply of 24 V (DC).

PINNING - SOT115J

PIN	DESCRIPTION
1	input
2	common
3	common
5	$+V_B$
7	common
8	common
9	output

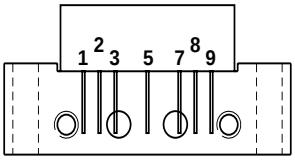


Fig.1 Simplified outline.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G_p	power gain	f = 10 MHz	29.2	30.8	dB
I_{tot}	total current consumption (DC)	V_B = 24 V	–	135	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_i	RF input voltage	–	55	dBmV
T_{stg}	storage temperature	–40	+100	°C
T_{mb}	operating mounting base temperature	–20	+100	°C

CHARACTERISTICS

Table 1 Bandwidth 5 to 75 MHz; V_B = +24 V; T_{mb} = 30 °C; $Z_S = Z_L$ = 75 Ω

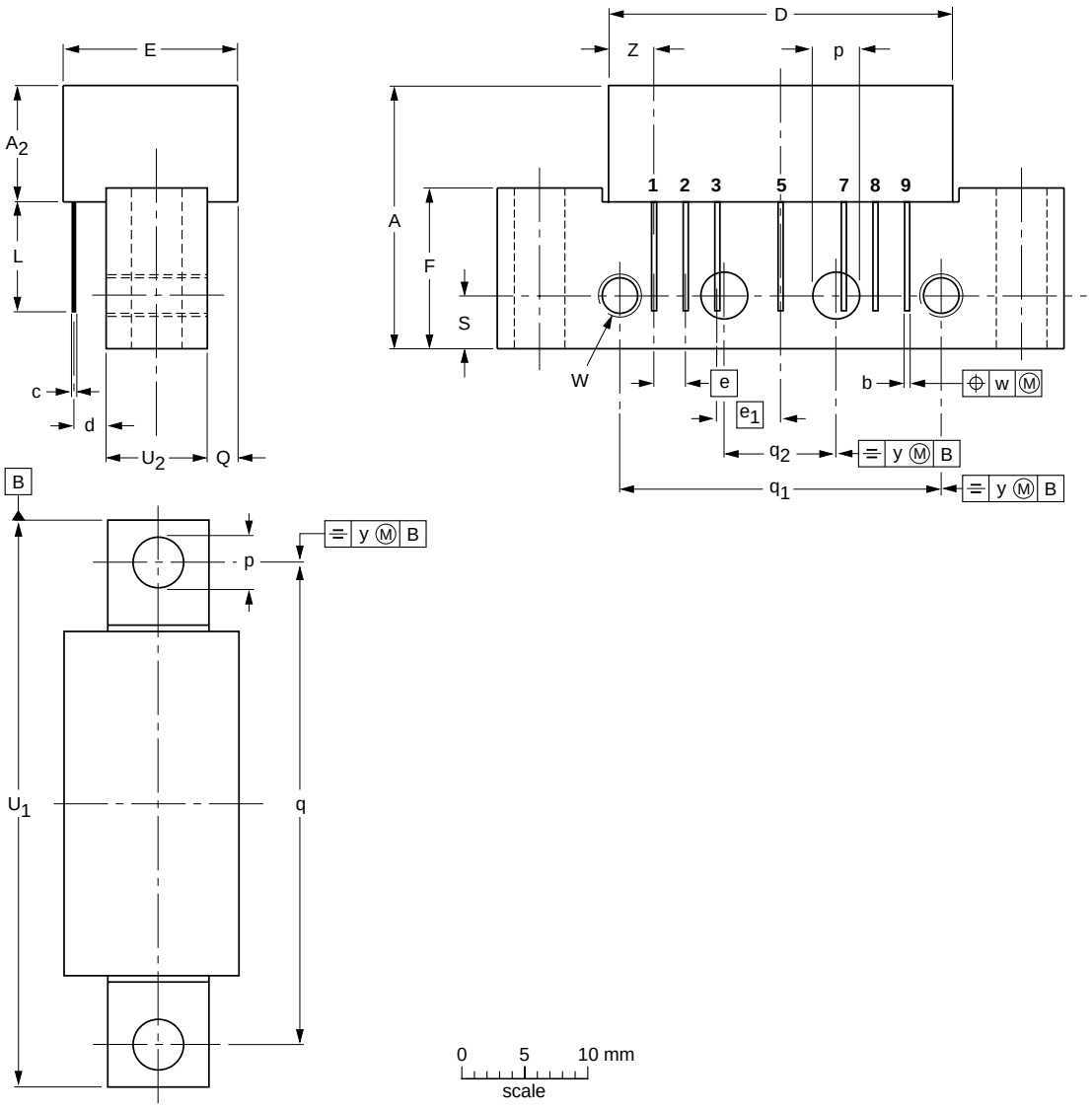
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G_p	power gain	f = 10 MHz	29.2	30.8	dB
SL	slope cable equivalent	f = 5 to 75 MHz	–0.2	+0.5	dB
FL	flatness of frequency response	f = 5 to 75 MHz	–	±0.2	dB
S_{11}	input return losses	f = 5 to 75 MHz	20	–	dB
S_{22}	output return losses	f = 5 to 50 MHz	20	–	dB
		f = 50 to 75 MHz	18	–	dB
CTB	composite triple beat	4 channels flat; V_o = 50 dBmV; measured at 25 MHz	–	–68	dB
X_{mod}	cross modulation	4 channels flat; V_o = 50 dBmV; measured at 25 MHz	–	–60	dB
d_2	second order distortion	note 1	–	–70	dB
F	noise figure	f = 75 MHz	–	3.5	dB
I_{tot}	total current consumption (DC)	note 2	–	135	mA

Notes

1. f_p = 19 MHz; V_p = 50 dBmV;
   f_q = 31 MHz; V_q = 50 dBmV;
   measured at $f_p + f_q$ = 50 MHz.
2. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads **SOT115J**



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A_2 max.	b	c	D max.	d max.	E max.	e	e_1	F	L min.	p	Q max.	q	q_1	q_2	S	U_1 max.	U_2	W	w	y	Z max.
mm	20.8	9.1	0.51 0.38	0.25	27.2	2.54	13.75	2.54	5.08	12.7	8.8	4.15 3.85	2.4	38.1	25.4	10.2	4.2	44.75	8	6-32 UNC	0.25	0.1	3.8

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT115J						99-02-06

DATA SHEET STATUS

DATA SHEET STATUS[1]	PRODUCT STATUS[2]	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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