



75232

LINEAR INTEGRATED CIRCUIT

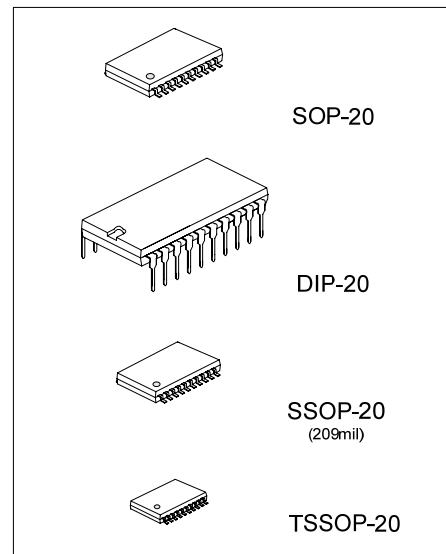
MULTIPLE RS-232 DRIVERS AND RECEIVERS

DESCRIPTION

The UTC 75232 is a monolithic IC containing three independent drivers and five receivers which comply with the requirements of the TIA/EIA232-F standards.

FEATURES

- * Single chip with easy interconnection of the UART and serial-port connector of personal computer.
- * Meets standard TIA/EIA-232-F and ITU V.28
- * Up to 120 kbps data rate
- * 20-pin SOP/ DIP/SSOP/TSSOP packages



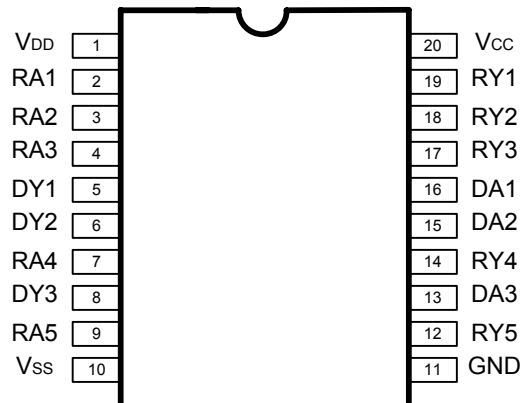
*Pb-free plating product number: 75232L

ORDERING INFORMATION

Ordering Number		Package	Packing
Normal	Lead Free Plating		
75232-D20-T	75232L-D20-T	DIP-20	Tube
75232-P20-R	75232L-P20-R	TSSOP-20	Tape Reel
75232-P20-T	75232L-P20-T	TSSOP-20	Tube
75232-R20-R	75232L-R20-R	SSOP-20	Tape Reel
75232-R20-T	75232L-R20-T	SSOP-20	Tube
75232-S20-R	75232L-S20-R	SOP-20	Tape Reel
75232-S20-T	75232L-S20-T	SOP-20	Tube

<p>75232L-D20-T</p> <p>(1)Packing Type (2)Package Type (3)Lead Plating</p>	<p>(1) R: Tape Reel, T: Tube (2) D20: DIP-20, S20: SOP-20, R20: SSOP-20, P20: TSSOP-20 (3) L: Lead Free Plating, Blank: Pb/Sn</p>
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■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	FUNCTION
1	V _{DD}	Supply Voltage
2	RA1	First Receiver Input
3	RA2	Second Receiver Input
4	RA3	Third Receiver Input
5	DY1	First Driver Output
6	DY2	Second Driver Output
7	RA4	Fourth Receiver Input
8	DY3	Third Driver Output
9	RA5	Fifth Receiver Input
10	V _{SS}	Supply Voltage
11	GND	Ground
12	RY5	Fifth Receiver Output
13	DA3	Third Driver Input
14	RY4	Fourth Receiver Output
15	DA2	Second Driver Input
16	DA1	First Driver Input
17	RY3	Third Receiver Output
18	RY2	Second Receiver Output
19	RY1	First Receiver Output
20	V _{CC}	Supply Voltage

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage (Note 2)		V_{DD}	15	V
		V_{SS}	-15	V
		V_{CC}	10	V
Input Voltage Range	Driver	V_{IN}	-15 ~ 7	V
	Receiver		-30 ~ 30	V
Driver Output Voltage Range		V_{OUT}	-15 ~ 15	V
Receiver Low Level Output Current		I_{OUT}	20	mA
Operation Temperature		T_{OPR}	0 ~ +70	°C
Storage Temperature		T_{STG}	-40 ~ +150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. All voltages are with respect to the network ground terminal.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Thermal Resistance Junction- Ambient	DIP-20	θ_{JA}	70	°C/W
	SOP-20		100	
	SSOP-20		115	
	TSSOP-20		115	

■ RECOMMENDED OPERATING RATINGS

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage		V_{DD}	7.5	9	15	V
		V_{SS}	-7.5	-9	-15	V
		V_{CC}	4.5	5	5.5	V
Level Input Voltage (driver only)	High	V_{IH}	1.9			V
	Low	V_{IL}			0.8	V
High Level Output Current	Driver	I_{OH}			-6.0	mA
	Receiver				-0.5	
Low Level Output Current	Driver	I_{OL}			6	mA
	Receiver				16	
Operating Free-air Temperature		T_{OPR}	0		70	°C

■ ELECTRICAL CHARACTERISTICS

(Over recommended supply voltage and operating temperature ranges, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
SUPPLY CURRENTS							
Supply Current from V_{DD}	I_{DD}	No load. All inputs at 1.9V	$V_{DD}=9V, V_{SS}=-9V$			15	mA
			$V_{DD}=12, V_{SS}=-12V$			19	
			$V_{DD}=15, V_{SS}=-15V$			25	
	No load. All inputs at 0.8V	$V_{DD}=9V, V_{SS}=-9V$			4.5	mA	
		$V_{DD}=12, V_{SS}=-12V$			5.5		
		$V_{DD}=15, V_{SS}=-15V$			9		
Supply Current from V_{SS}	I_{SS}	No load. All inputs at 1.9V	$V_{DD}=9V, V_{SS}=-9V$			-15	mA
			$V_{DD}=12, V_{SS}=-12V$			-19	
			$V_{DD}=15, V_{SS}=-15V$			-25	
	No load. All inputs at 0.8V	$V_{DD}=9V, V_{SS}=-9V$			-3.2	mA	
		$V_{DD}=12, V_{SS}=-12V$			-3.2		
		$V_{DD}=15, V_{SS}=-15V$			-3.2		
Supply Current from V_{CC}	I_{CC}	No load. All inputs at 5V, $V_{CC}=5V$			30	mA	

■ ELECTRICAL CHARACTERISTICS(Cont.)

DRIVER SECTION ($V_{DD}=9V$, $V_{SS}=-9V$, $V_{CC}=5V$)								
Output Voltage Level (Figure. 1)	High	V_{OH}	$V_{IL}=0.8V$, $R_L=3\text{ k}\Omega$	6	7.5		V	
	Low	V_{OL}	$V_{IH}=1.9V$, $R_L=3\text{ k}\Omega$ (Note 1)		-7.5	-6	V	
Input Current Level (Figure. 2)	High	I_{IH}	$V_{IN}=5V$			10	μA	
	Low	I_{IL}	$V_{IN}=0V$			-1.6	mA	
Short Circuit Output Current (Figure. 1)	High	$I_{OS(H)}$	$V_{IL}=0.8V$, $V_{OUT}=0V$ (Note 2)	-8.5	-12	-19.5	mA	
	Low	$I_{OS(L)}$	$V_{IH}=2.0V$, $V_{OUT}=0V$	8.5	12	19.5	mA	
Output Resistance (Note 3)		R_{OUT}	$V_{DD}=V_{SS}=V_{CC}=0V$, $V_{OUT}=-2V\sim 2V$	300			Ω	
RECEIVER SECTION ($V_{DD}=9V$, $V_{SS}=-9V$, $V_{CC}=5V$)								
Going Threshold Voltage	Positive	V_{T+}	$T_a=25^\circ C$ (Figure. 5)	1.75	1.9	2.3	V	
			$T_a=0^\circ C \sim 70^\circ C$ (Figure. 5)	1.55		2.3		
	Negative	V_{T-}		0.75	0.97	1.25	V	
Input Hysteresis($V_{T+} - V_{T-}$)		V_{HYS}		0.5			V	
Output Voltage Level	High	V_{OH}	$I_{OH}=-0.5mA$	$V_{IH}=0.75V$	2.6	4	5	V
				Inputs Open	2.6			
	Low	V_{OL}	$V_{IN}=3V$, $I_{OL}=10mA$		0.2	0.45	V	
Input Current Level (Figure. 5)	High	I_{IH}	$V_{IN}=25V$	3.6		8.3	mA	
			$V_{IN}=3V$	0.43				
	Low	I_{IL}	$V_{IN}=-25V$	-3.6		-8.3	mA	
			$V_{IN}=-3V$	-0.43				
Short-Circuit Output Current		I_{OS}	(Fig. 4)		-3.4	-12	mA	

Note. 1. For voltage logic levels, the more positive (less negative) limit is designated as maximum, (e.g. if -10V is a maximum, the typical value is a more negative voltage).

2: Output short circuit conditions must maintain the total power dissipation below absolute maximum ratings and the sign is used to indicate direction.

3: Test conditions are those specified by TIA/EIA232-F and as listed above.

■ SWITCHING CHARACTERISTICS ($T_a=25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
DRIVER SECTION ($V_{DD}=12V$, $V_{SS}=-12V$, $V_{CC}=5V$)							
Propagation Delay Time	Low to High	t_{PLH}	$R_L=3 \sim 7\text{ k}\Omega$, $C_L=15pF$ (Figure. 3)		315	500	ns
	High to low	t_{PHL}			75	175	ns
Transition Time	Low to High	t_{TLH}	$R_L=3 \sim 7\text{ k}\Omega$, $C_L=15pF$		60	100	ns
	High to Low	t_{THL}			40	75	ns
	Low to High	t_{TLH}	$R_L=3 \sim 7\text{ k}\Omega$, $C_L=2500pF$ (Figure. 3, Note)		1.7	2.5	μs
	High to Low	t_{THL}			1.5	2.5	μs
RECEIVER SECTION ($V_{DD}=12V$, $V_{SS}=-12V$, $V_{CC}=5V$)							
Propagation Delay Time (Figure. 6)	Low to High	t_{PLH}	$R_L=5\text{ k}\Omega$, $C_L=50pF$		107	250	ns
	High to low	t_{PHL}			42	150	ns
	Low to High	t_{PLH}	$R_L=1.5\text{ k}\Omega$, $C_L=15pF$		100	160	ns
	High to Low	t_{PHL}			60	100	ns
Transition Time (Figure. 6)	Low to High	t_{TLH}	$R_L=5\text{ k}\Omega$, $C_L=50pF$		175	350	ns
	High to Low	t_{THL}			16	60	ns
	Low to High	t_{TLH}	$R_L=1.5\text{ k}\Omega$, $C_L=15pF$		90	175	ns
	High to Low	t_{THL}			15	50	ns

Note: Measured points of the output waveform (TIA/EIA-232-F conditions) as below, all unused inputs are tied.

- Low to High level: between -3V and 3V.

- High to Low level: between 3V and -3V.

PARAMETER MEASUREMENT INFORMATION

DRIVER TEST CIRCUITS:

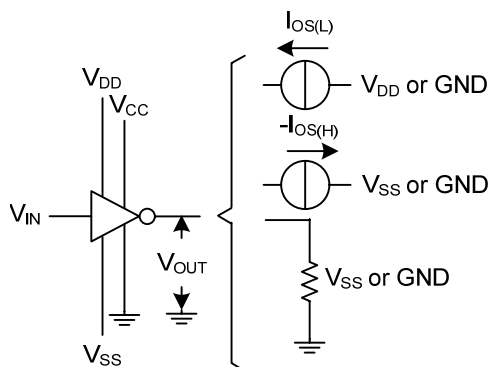


Figure. 1 For V_{OH}, V_{OL}, I_{OS(H)}, I_{OS(L)}

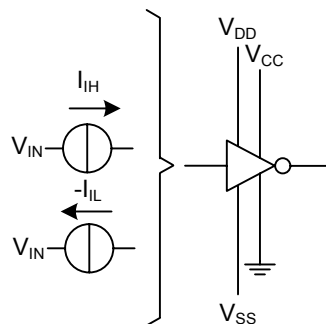


Figure. 2 For I_{IH}, I_{IL}

DRIVER VOLTAGE WAVEFORMS:

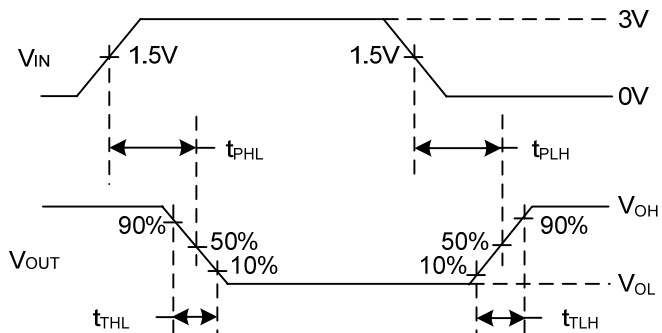
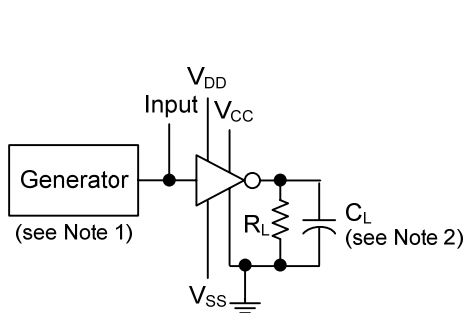


Figure. 3

Note: 1. The pulse generator characteristics: $t_w=25\mu s$, $PRR=20kHz$, $Z_o=50\Omega$, $t_r=t_f<50ns$.
 2. C_L includes probe and jig capacitance.

PARAMETER MEASUREMENT INFORMATION (Cont.)

RECEIVER TEST CIRCUITS:

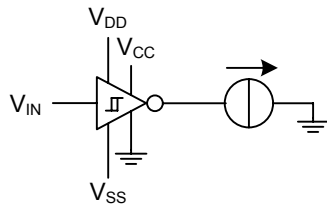


Figure 4. Ios

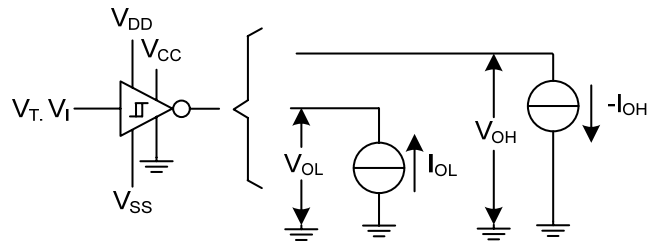


Figure 5 VT, VoH, VoL

RECEIVER PROPAGATION AND TRANSITION TIMES:

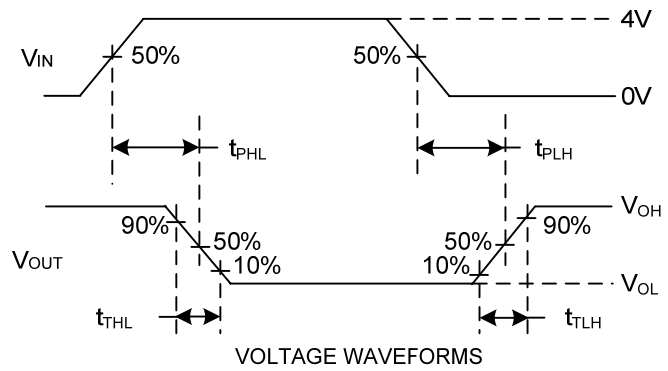
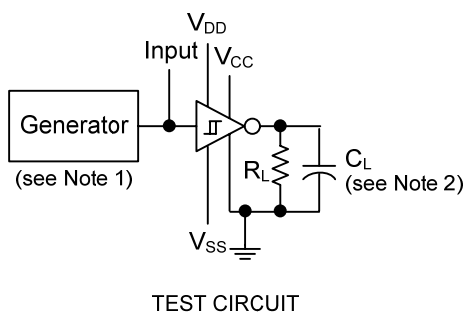


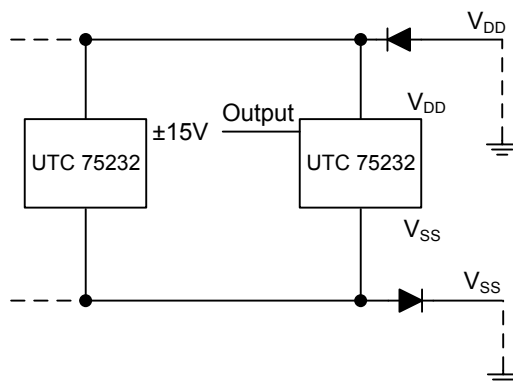
Figure 6.

Note: 1. The pulse generator characteristics: $t_w=25\mu s$, $PRR=20kHz$, $Z_o=50\Omega$, $t_r=t_f<50ns$.
 2. C_L includes probe and jig capacitance.

APPLICATION INFORMATION

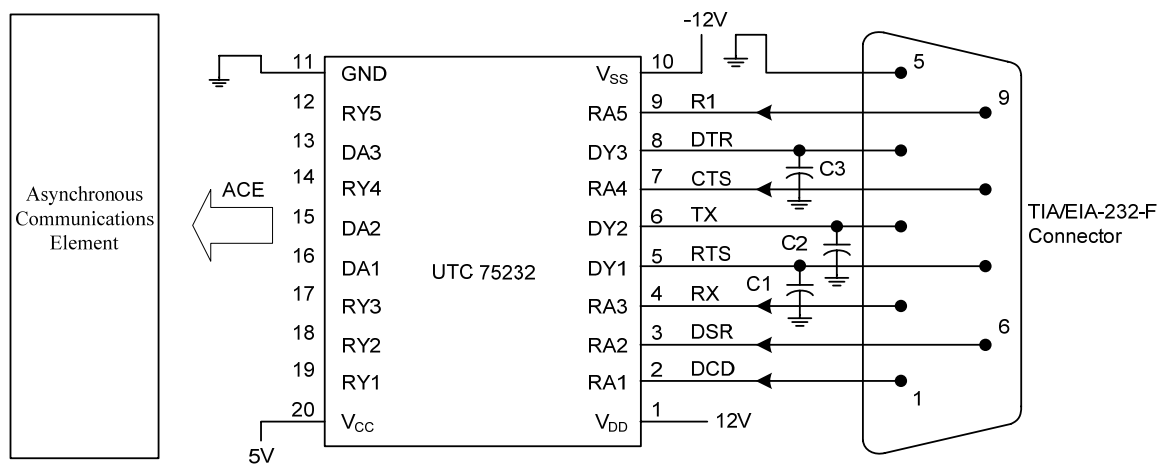
Power-Supply protection to meet Power-Off fault conditions of TIA/EIA-232-F

Diodes placed in series with the V_{DD} and V_{SS} leads protect the device in the fault condition in which the device outputs are shorted to $\pm 15V$ and the power supplies are at low and provide low-impedance paths to ground.

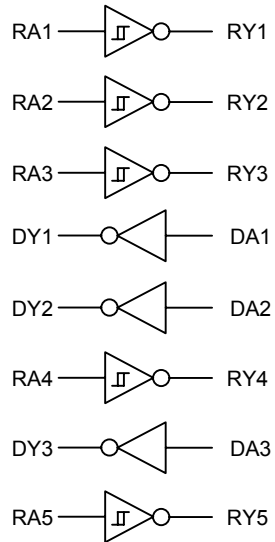
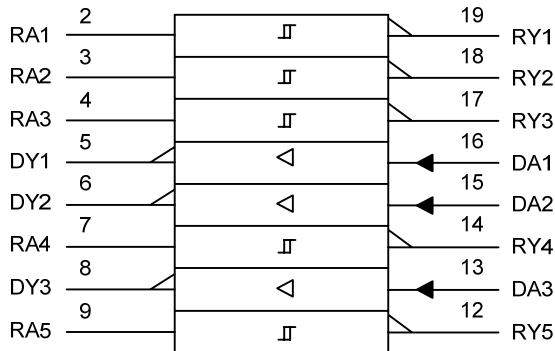


Typical Connection

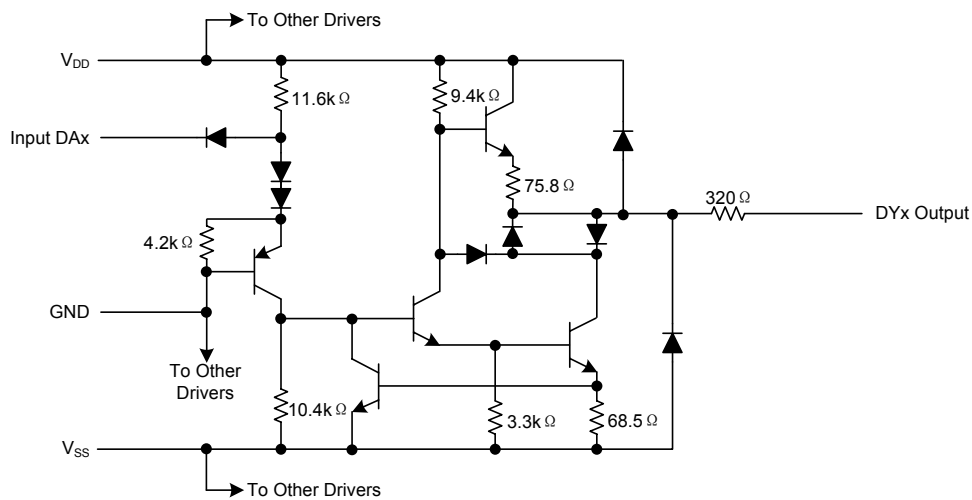
“*”: Refer Figure 10 to select the correct values for the loading capacitors (C1, C2, and C3), which are required to meet the RS-232 maximum slew-rate requirement of $30V/\mu s$. The value of the loading capacitors required depends upon the line length and desired slew rate, but typically is 330 pF.



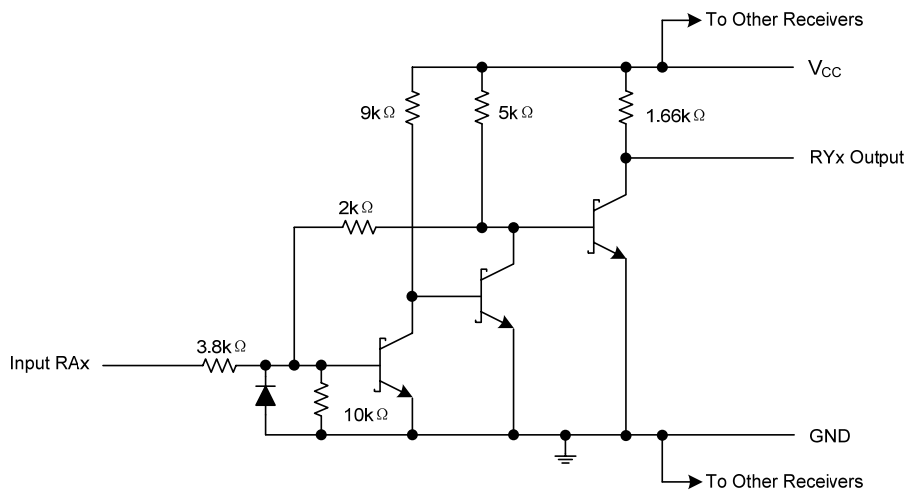
LOGIC SYMBOL AND LOGIC DIAGRAM



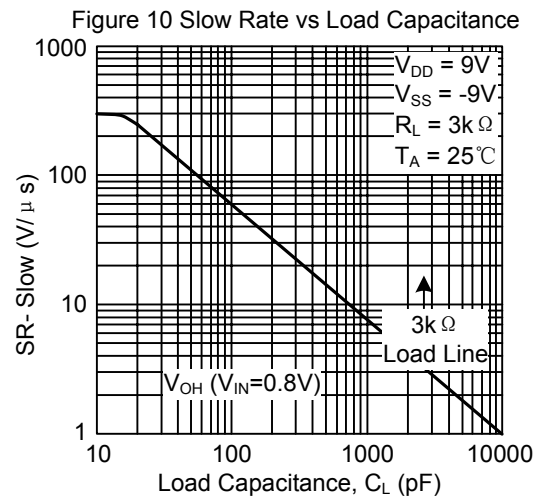
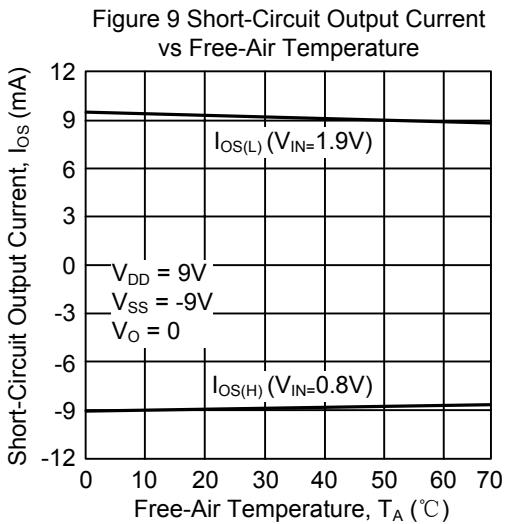
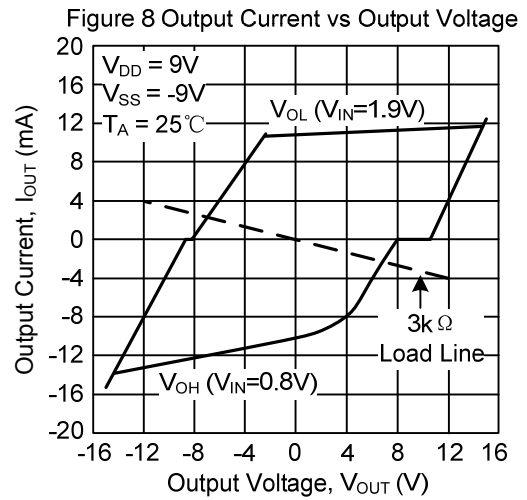
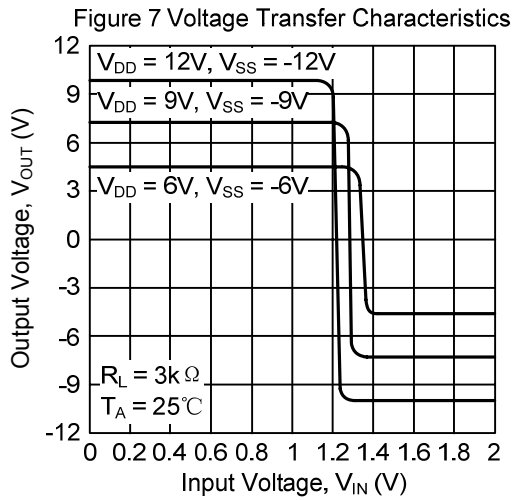
CIRCUIT OF DRIVERS (Resistor value shown are nominal.)



CIRCUIT OF EACH RECEIVER (Resistor value shown are nominal.)



■ TYPICAL CHARACTERISTICS(DRIVER)



■ TYPICAL CHARACTERISTICS(RECEIVER)

Figure 11 Input Threshold Voltage vs Free-Air Temperature

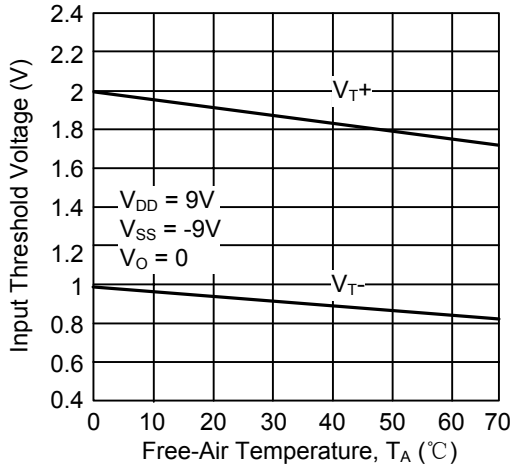


Figure 12 Input Threshold Voltage vs Supply Voltage

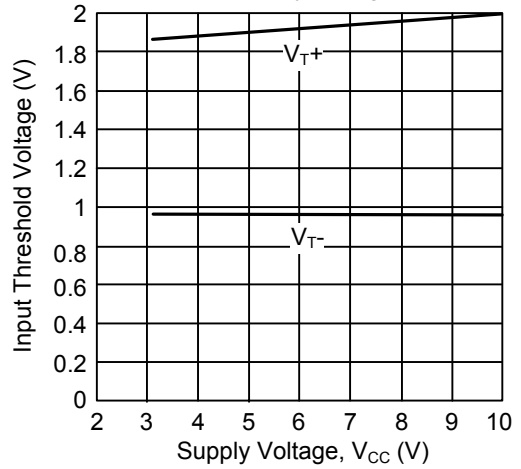
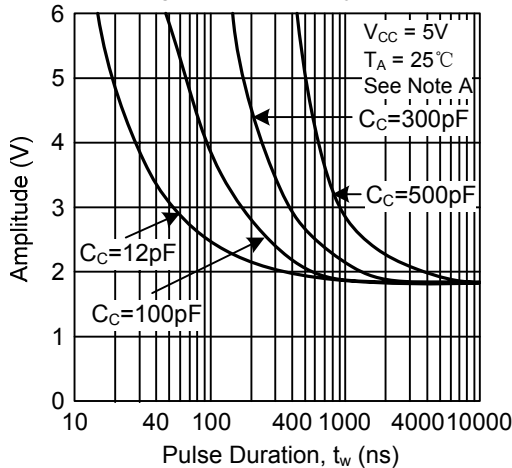
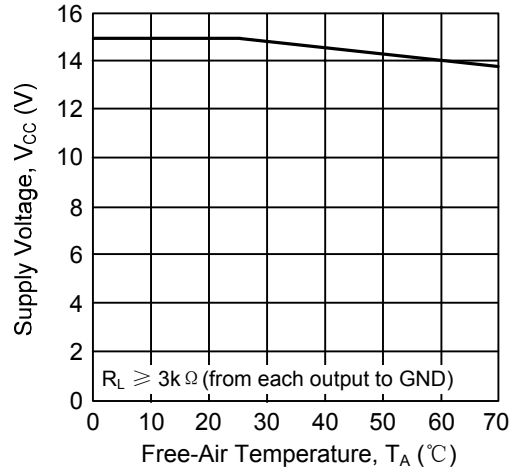


Figure 13 Noise Rejection



The maximum amplitude starting from 0V of a positive-going pulse that will not cause a change in the output level.

Figure 14 Maximum Supply Voltage vs Free-Air Temperature



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