

SBR3U30P1

# 3.0A SBR<sup>®</sup> Super Barrier Rectifier PowerDI<sup>™</sup>123

#### Features Mechanical Data

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- ±16KV ESD Protection (HBM, 3B)
- ±25KV ESD Protection (IEC61000-4-2 Level 4, Air Discharge)
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q 101 Standards for High Reliability

- Case: PowerDI™123
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity Indicator: Cathode Band
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 4
- Ordering Information: See Page 4

#### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>RM</sub>	30	V
RMS Reverse Voltage	$V_{R(RMS)}$	21	V
Average Rectified Output Current (See Figure 1)	Io	3.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	75	А
Non-Repetitive Avalanche Energy ( $T_J = 25$ °C, $I_{AS} = 5A$ , $L = 8.5$ mH)	E <sub>AS</sub>	105	mJ
Repetitive Peak Avalanche Energy (1µs, 25°C)	P <sub>ARM</sub>	1100	W
Maximum Thermal Resistance Thermal Resistance Junction to Soldering (Note 2) Thermal Resistance Junction to Ambient (Note 3) Thermal Resistance Junction to Ambient (Note 4)	R <sub>eJS</sub> R <sub>eJA</sub>	5 178 123	°C/W
Operating and Storage Temperature Range (Note 5)	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

Notes:

- 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see *EU Directive Annex Note* 7.
- 2. Theoretical R<sub>eus</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf

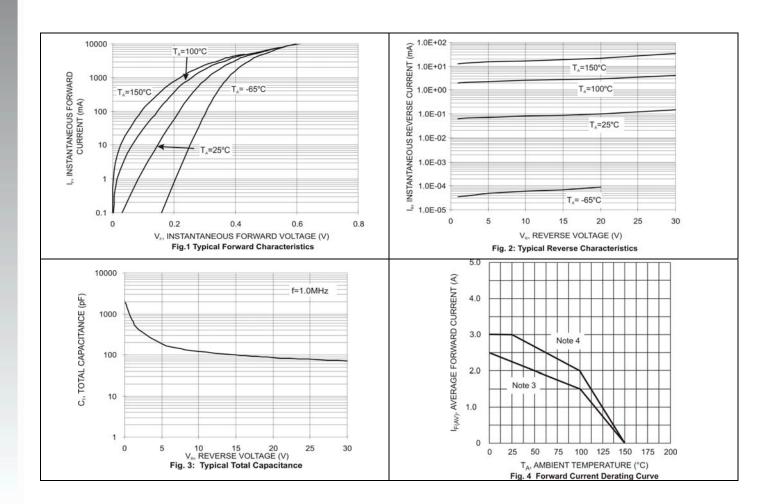


## Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	$V_{(BR)R}$	30	-	-	V	I <sub>R</sub> = 400 μA
		-	0.28	0.32		$I_F = 0.5A, T_J = 25^{\circ}C$
	V <sub>F</sub>		0.31	0.35		$I_F = 1.0A, T_J = 25^{\circ}C$
Forward Voltage Drop			0.39	0.43	V	$I_F = 3.0A, T_J = 25^{\circ}C$
			0.20	0.23		$I_F = 0.5A, T_J = 125^{\circ}C$
			0.23	0.26		$I_F = 1.0A, T_J = 125^{\circ}C$
			0.35	0.38		$I_F = 3.0A, T_J = 125^{\circ}C$
	I <sub>R</sub>	-	70	150	μA	V <sub>R</sub> = 5V, T <sub>J</sub> = 25 °C
Lookono Current (Noto E)			150	400	μA	$V_R = 30V, T_J = 25  {}^{\circ}C$
Leakage Current (Note 5)			6	15	mA	V <sub>R</sub> = 5V, T <sub>J</sub> = 125 °C
			12	20	mA	V <sub>R</sub> = 30V, T <sub>J</sub> = 125 °C

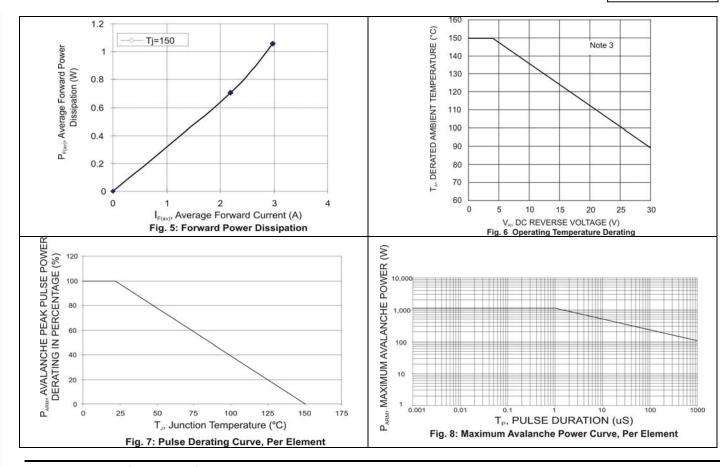
Notes:

5. Short duration pulse test used to minimize self-heating effect.



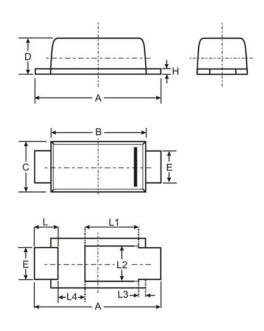


### **SBR3U30P1**



# **Package Outline Drawings**

## PowerDI™123



PowerDI <sup>™</sup> 123									
Dim	Min	Max	Тур						
Α	3.65	3.75	3.70						
В	2.775	2.825	2.80						
С	1.750	1.800	1.775						
D	0.955	1.000	0.98						
Е	0.95	1.05	1.00						
н	0.15	0.25	0.20						
L	0.60	0.70	0.65						
L1	_	_	1.36						
L2	_	_	1.10						
L3			0.20						
L4	0.95	1.25	1.05						
All Dimensions in mm									



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## Marking, Polarity, Weight & Ordering Information

2	Case	Style	Marking	Weight		
SBR3U30F	Top View	Back View	[]3U3 ₹	0.096g (approx.)		

Ordering Information	Date Code
SBR3U30P1-7 3000/Tape & Reel	3U3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

#### Date Code Key

Date code key													
Year	2006		2007		2008		2009		2010	2	2011	20	)12
Code	Т		U		V		W	W X		Y		Z	
Month J		Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Code	1	2	3	4	5	6	7	8	9	0	N	D

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