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## **NTE2050 & NTE2051 Integrated Circuit 3<sup>1</sup>/<sub>2</sub>-Digit Single Chip A/D Converter**

### **Description:**

The NTE2050 and NTE2051 are high performance, low power 3-1/2 digit A/D converters containing all the necessary active devices on a single CMOS IC. Included are seven segment decoders, display drivers, reference and a clock. The NTE2051 is designed to interface with a liquid crystal display (LCD) and includes a backplane drive; the NTE2050 will directly drive an instrument-size light emitting diode (LED) display.

The NTE2050 and NTE2051 bring together an unprecedented combination of high accuracy, versatility, and true economy. High accuracy like auto-zero to less than 10 $\mu$ V, zero drift of less than 1 $\mu$ V/ $^{\circ}$ C, input bias current of 10pA max., and roll-over error of less than one count. The versatility of true differential input and reference is useful in all systems, but gives the designer an uncommon advantage when measuring load cells, strain gauges and other bridge-type transducers. And, finally, the true economy of single power supply operation (NTE2051), enabling a high performance panel meter to be built with the addition of only 7 passive components and a display.

### **Features:**

- Guaranteed Zero Reading for 0 Volts Input on all Scales
- True Polarity at Zero for Precise Null Detection
- Input Current: 1pA Typ
- True Differential Input and Reference
- Direct Display Drive – No External Components Required:  
NTE2050 (LED)  
NTE2051 (LCD)
- Low Noise – Less than 15 $\mu$ V<sub>P-P</sub>
- On-Chip Clock and Reference
- Low Power Dissipation – Typically Less than 10mW
- No Additional Active Circuits Required

**Absolute Maximum Ratings:**

Supply Voltage,	
NTE2051, V+ to V–	15V
NTE2050, V+ to GND	+6V
NTE2050, V– to GND	–9V
Analog Input Voltage (Either Input, Note 2)	V+ to V–
Reference Input Voltage (Either Input)	V+ to V–
Clock Input,	
NTE2051	TEST to V+
NTE2050	GND to V+
Power Dissipation (Note 3)	800mW
Operating Temperature Range	0° to +70°C
Storage Temperature Range	–65° to +160°C
Lead Temperature (During Soldering, 60sec)	+300°C

Note 1. Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or another conditions above those indicated in the operational section of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2. Input voltages may exceed the supply voltages provided the input current is limited to ±100µA.

Note 3. Dissipation rating assumes device is mounted with all leads soldered to printed circuit board.

**Electrical Characteristics:** (T<sub>A</sub> = +25°C, f<sub>clock</sub> = 48kHz unless otherwise specified)

Parameter	Test Conditions	Min	Typ	Max	Unit
Zero Input Reading	V <sub>IN</sub> = 0V Full Scale = 200mV	–000.0	±000.0	+000.0	Digital Reading
Ratiometric Reading	V <sub>IN</sub> = V <sub>REF</sub> V <sub>REF</sub> = 100mV	999	999/1000	1000	Digital Reading
Rollover Error (Difference in Reading for Equal Positive and Negative Reading near Full Scale)	–V <sub>IN</sub> = +V <sub>IN</sub> □ 200mV	–1	±.2	+1	Counts
Linearity (Maximum Deviation from Best Straight Line Fit)	Full Scale = 200mV or Full Scale = 2V	–1	±.2	+1	Counts
Common Mode Rejection Ratio	V <sub>CM</sub> = ±1V, V <sub>IN</sub> = 0V Full Scale = 200mV	–	50	–	µV/V
Noise (Peak–Peak Value not Exceeded 95% of Time)	V <sub>IN</sub> = 0V Full Scale = 200mV	–	15	–	µV
Leakage Current   Input	V <sub>IN</sub> = 0	–	1	10	pA
Zero Reading Drift	V <sub>IN</sub> = 0, 0° < T <sub>A</sub> < +70°C	–	0.2	1.0	µV/°C
Scale Factor Temperature Coefficient	V <sub>IN</sub> = 199.0mV 0° < T <sub>A</sub> < +70°C (Ext. Ref. 0ppm/°C)	–	1	5	ppm/°C
V+ Supply Current (Does Not Include LED Current for NTE2050)	V <sub>IN</sub> = 0	–	0.8	1.8	mA
V– Supply Current (NTE2050 Only)		–	0.6	1.8	mA

**Electrical Characteristics (Cont'd):** ( $T_A = +25^\circ\text{C}$ ,  $f_{\text{clock}} = 48\text{kHz}$  unless otherwise specified)

Parameter	Test Conditions	Min	Typ	Max	Unit
Analog Common Voltage Common (With Respect to Positive Supply)	25k $\Omega$ Between Common & Positive Supply	2.4	2.8	3.2	V
Temperature Coefficient of Analog Common (With Respect to Positive Supply)	25k $\Omega$ Between Common & Positive Supply	–	80	–	ppm/ $^\circ\text{C}$
Peak–Peak Segment Drive Voltage, Peak–Peak Backplane Drive Voltage (NTE2051 Only)	$V_+$ to $V_- = 9\text{V}$ , Note 4	4	5	6	V
Segment Sinking Current (NTE2050 Only) (Except Pin19) (Pin19 Only)	$V_+ = 5\text{V}$ Segment Voltage = 3V	5 10	8 16	– –	mA

Note 4. Back plane drive is in phase with segment drive for “off” segment,  $180^\circ$  out of phase for “on” segment. Frequency is 20 times conversion rate. Average DC component is less than 50mV.

**Pin Connection Diagram**

Units V(+)	<b>1</b>	<b>40</b>	OSC 1
Units D1	<b>2</b>	<b>39</b>	OSC 2
Units C1	<b>3</b>	<b>38</b>	OSC 3
Units B1	<b>4</b>	<b>37</b>	Test
Units A1	<b>5</b>	<b>36</b>	Ref High
Units F1	<b>6</b>	<b>35</b>	Ref Low
Units G1	<b>7</b>	<b>34</b>	C (+) Ref
Units E1	<b>8</b>	<b>33</b>	C (-)
Tens D2	<b>9</b>	<b>32</b>	Common
Tens C2	<b>10</b>	<b>31</b>	In High
Tens B2	<b>11</b>	<b>30</b>	In Low
Tens A2	<b>12</b>	<b>29</b>	A/Z
Tens F2	<b>13</b>	<b>28</b>	Buff
Tens E2	<b>14</b>	<b>27</b>	Int
100's D3	<b>15</b>	<b>26</b>	V (-)
100's B3	<b>16</b>	<b>25</b>	G2 Tens
100's F3	<b>17</b>	<b>24</b>	C3 100's
100's E3	<b>18</b>	<b>23</b>	A3 100's
AB4	<b>19</b>	<b>22</b>	G3 100's
Polarity (-)	<b>20</b>	<b>21</b>	BP/GND*

**Note:** Pin21 is GND on the NTE2050

