SIEMENS

**Multichannel Network Interface Controller for HDLC
MUNICH32**

PEB/F 20320 Version 3.3
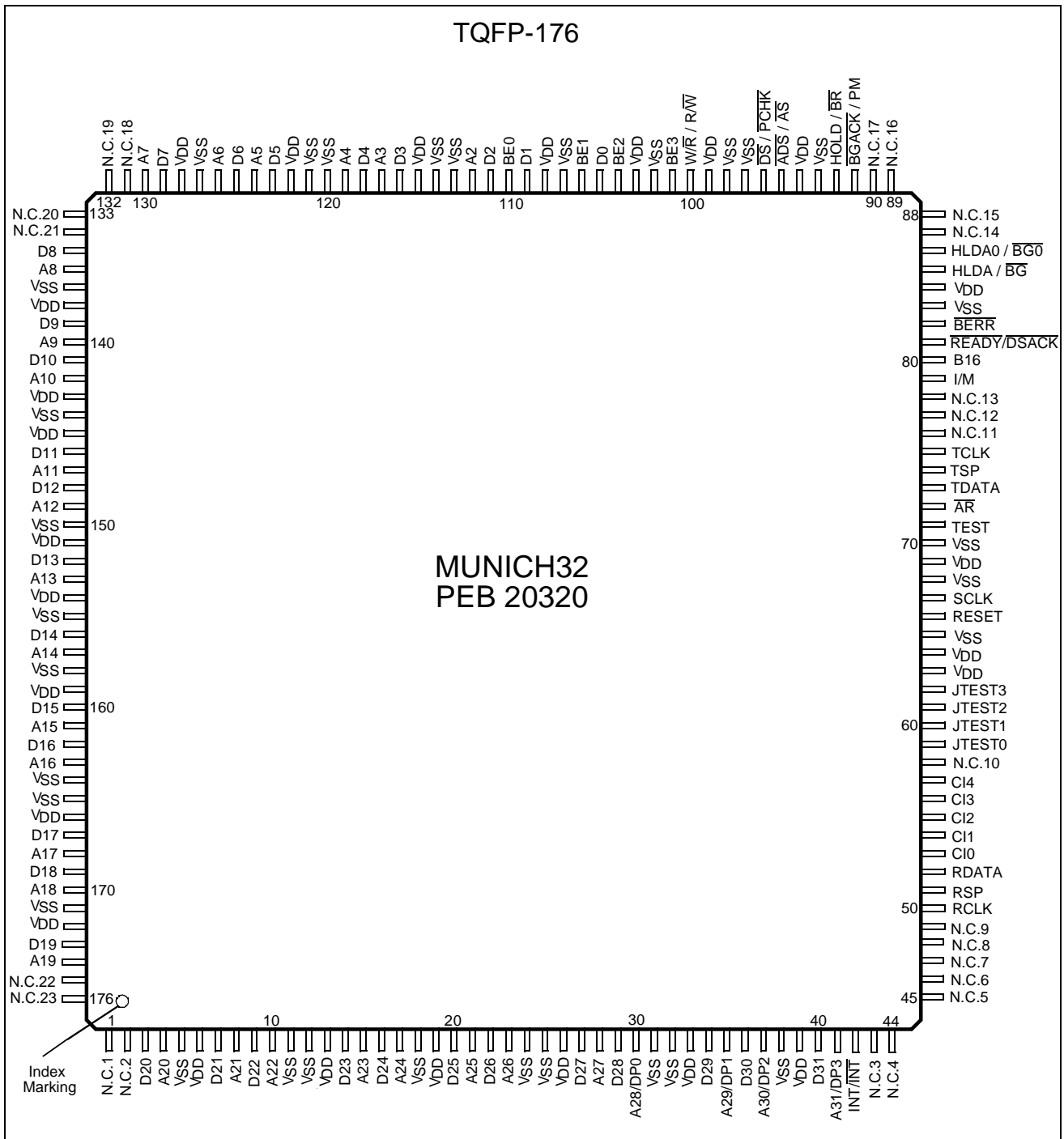
Delta Sheet 12.97

This document describes the latest MUNICH32 (Version 3.3) in relation to the Version 3.2 and hence is a "Delta Sheet" which references the MUNICH32 User's Manual 01.96.

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1 Upgrades of MUNICH32 V3.3

1.1 Package

MUNICH32 V3.3 is provided in both packages: P-MQFP-160-1 and P-TQFP-176.

1.2 Pin Configuration
(top view)

TQFP-176

MUNICH32
PEB 20320

1.3 Functional Upgrades

The RESET behaviour was changed between MUNICH32 V3.2 and MUNICH32 V3.3:

MUNICH32 V3.3 performs a reset sequence in the first 12 SCL cycles after the leading edge of the 'RESET' signal.
In order to meet this requirement the 'RESET' pin has to be logical '0' until power as well as the clock system are in a stable condition.
When being in this stable condition, the M32 is able to detect a rising edge of the 'RESET' signal, which starts the internal 'RESET' procedure.
This sequence is performed independent of the duration of the 'RESET' signal.
It is not allowed to activate an 'AR' during that reset sequence.

In case of a power up RESET the device would not detect a rising edge of the RESET signal and would not execute a RESET. To avoid this situation, the RESET signal could be inverted in systems using a power up RESET.
In this case the MUNICH32 V3.3 detects the trailing edge of RESET as 0 ->1 transition and performes the reset sequence as described above. In this case it is not allowed to give an AR during the first 12 SCL cycles after the trailing edge of RESET.

1.4 Electrical Characteristics

Note: The version number is identified in the Interrupt Information bits VN(3:1): these bits are '101' for version 3.3

The MUNICH32 V3.3 is available for the extended range of temperature:

Ambient temperature ratings

Parameter	Symbol	Limit Values	Unit
Ambient temperature under bias: PEB	T_A	0 to 70	°C
PEF	T_A	-40 to 85	°C

All other electrical characteristics are as specified in MUNICH32 User's Manual 01.96.