TOSHIBA PHOTOCOUPLER GaAℓAs IRED + PHOTO-IC

TLP559(IGM)

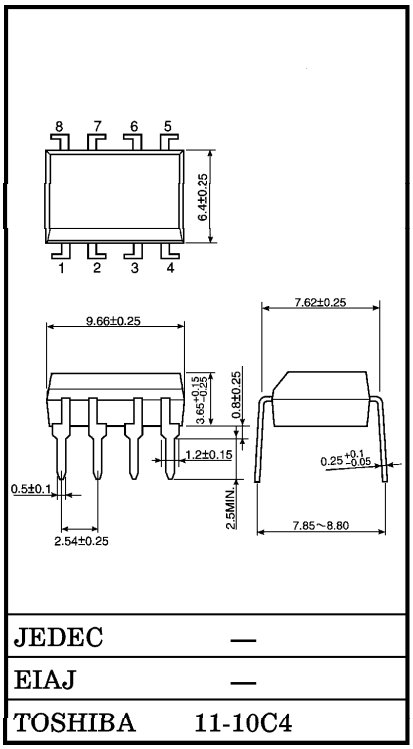
TRANSISTOR INVERTOR

INVERTER FOR AIR CONDITIONER

LINE RECEIVER

IPM INTERFACES

Unit in mm



JEDEC	—
EIAJ	—
TOSHIBA	11-10C4

Weight : 0.54g

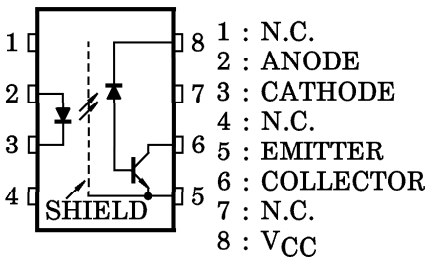
The TOSHIBA TLP559 (IGM) consists of a GaAℓAs high-output light emitting diode and a high speed detector of one chip photo diode-transistor.
This unit is 8-lead DIP package.

TLP559 (IGM) has no internal base connection, and a Faraday shield integrated on the photodetector chip provides an effective common mode noise transient immunity.

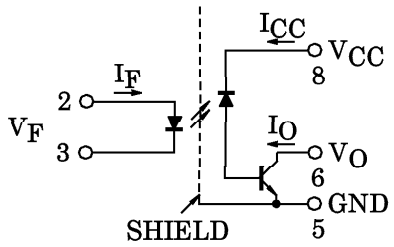
TLP559 (IGM) guarantees minimum and maximum of propagation delay time, switching time dispersion, and high common mode transient immunity. Therefor TLP559 (IGM) is suitable for isolation interface between IPM (Intelligent Power Module) and control IC circuits in motor control application.

- Isolation Voltage : 2500V_{rms} Min.
- Common Mode Transient Immunity
  : ±10kV / μs Min.
  @ V_{CM}=1500V
- Switching Time
  : t_{pHL}, t_{pLH}=0.1μs Min.
  =0.8μs Max.
  @ IF=10mA, V_{CC}=15V, R_L=20kΩ, Ta=25°C
- Switching Time Dispersion : 0.7μs Max.
  ($|t_{pLH}-t_{pHL}|$)
- TTL Compatible
- UL Recognized : UL1577, Fle No.E67349

PIN CONFIGURATION (Top view)



SCHEMATIC



961001EAC2

MAXIMUM RATINGS (Ta = 25°C)

	CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	(Note 1)	I_F	25	mA
	Pulse Forward Current	(Note 2)	I_{FP}	50	mA
	Peak Transient Forward Current	(Note 3)	I_{FPT}	1	A
	Reverse Voltage		V_R	5	V
	Diode Power Dissipation	(Note 4)	P_D	45	mW
Detector	Output Current		I_O	8	mA
	Peak Output Current		I_{OP}	16	mA
	Output Voltage		V_O	−0.5~20	V
	Supply Voltage		V_{CC}	−0.5~30	V
	Output Power Dissipation	(Note 5)	P_O	100	mW
Operating Temperature Range			T_{opr}	−55~100	°C
Storage Temperature Range			T_{stg}	−55~125	°C
Lead Solder Temperature (10s)		(Note 6)	T_{sol}	260	°C
Isolation Voltage (AC, 1min., R.H.≦60%, Ta=25°C)		(Note 7)	BV_S	2500	V_{rms}

(Note 1) : Derate 0.8mA above 70°C.
(Note 2) : 50% duty cycle, 1ms pulse width.
Derate 1.6mA / °C above 70°C.
(Note 3) : Pulse width PW≦1μs, 300pps.
(Note 4) : Derate 0.9mW / °C above 70°C.
(Note 5) : Derate 2mW / °C above 70°C.
(Note 6) : Soldering portion of lead : up to 2mm from the body of the device.
(Note 7) : Device considerd a two terminal device : pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

961001EAC2'

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
LED	Forward Voltage	V_F	I_F = 16mA	—	1.65	1.85	V
	Forward Voltage Temperature Coefficient	$\Delta V_F / \Delta Ta$	I_F = 16mA	—	−2	—	mV / °C
	Reverse Current	I_R	V_R = 5V	—	—	10	μA
	Capacitance between Terminal	C_T	V_F = 0, f = 1MHz	—	45	—	pF
Detector	High Level Output Current	I_{OH} (1)	I_F = 0mA, V_{CC} = V_O = 5.5V	—	3	500	nA
		I_{OH} (2)	I_F = 0mA, V_{CC} = 30V, V_O = 20V	—	—	5	μA
		I_{OH}	I_F = 0mA, V_{CC} = 30V, V_O = 20V, Ta = 70°C	—	—	50	
	High Level Supply Voltage	I_{CCH}	I_F = 0mA, V_{CC} = 30V	—	0.01	1	μA
	Supply Voltage	V_{CC}	I_{CC} = 0.01mA	30	—	—	V
	Output Voltage	V_O	I_O = 0.5mA	20	—	—	V

COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Current Transfer Ratio	I_O / I_F	I_F = 10mA, V_{CC} = 4.5V, V_O = 0.4V	25	35	75	%
		I_F = 16mA, V_{CC} = 4.5V, V_O = 0.4V, Ta = −25~100°C	15	—	—	
Low Level Output Voltage	V_{OL}	I_F = 16mA, V_{CC} = 4.5V, I_O = 2.4mA	—	—	0.4	V

ISOLATION CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Capacitance Input to Output	C_S	V = 0, f = 1MHz (Note 8)	—	0.8	—	pF
Isolation Resistance	R_S	R.H. ≦ 60%, V_S = 500V (Note 8)	5×10^{10}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 1 minute	2500	—	—	V_{rms}
		AC, 1 second, in oil	—	5000	—	Vdc
		DC, 1 minute, in oil	—	5000	—	

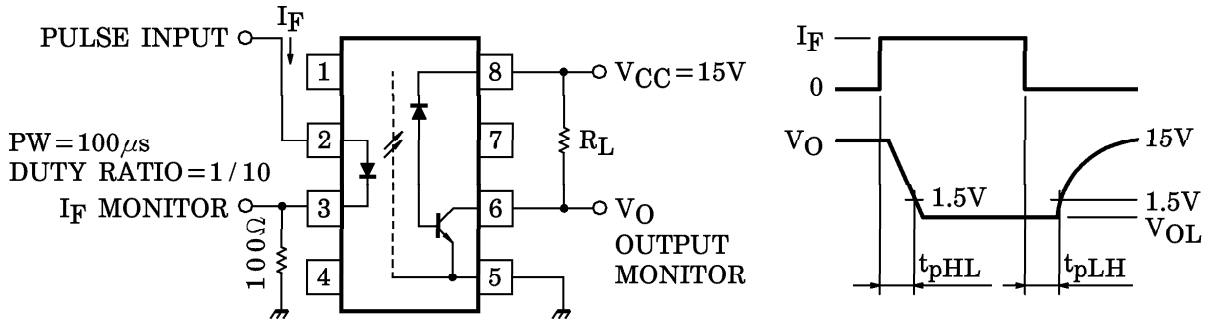
SWITCHING CHARACTERISTICS (Ta = 25°C, V_{CC} = 15V)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time (H→L)	t_{pHL}	1	I_F=0→10mA, R_L=20kΩ	0.1	0.45	0.8	μs
			I_F=0→10mA, R_L=20kΩ Ta=0~85°C	0.1	0.45	0.9	
Propagation Delay Time (L→H)	t_{pLH}		I_F=0→10mA, R_L=20kΩ Ta=−25~100°C	0.1	0.45	1.0	
Switching Time Dispersion between ON and OFF	$\lvert t_{pLH}-t_{pHL}\rvert$		I_F=10→0mA, R_L=20kΩ	—	0.15	0.7	μs
			I_F=10→0mA, R_L=20kΩ Ta=0~85°C	—	0.25	0.8	
			I_F=10→0mA, R_L=20kΩ Ta=−25~100°C	—	0.25	0.9	
Common Mode Transient Immunity at Logic High Output (Note 8)	CM_H	2	I_F=0mA V_{CM}=1500V_{p-p} R_L=20kΩ	10000	15000	—	V / μs
Common Mode Transient Immunity at Logic Low Output (Note 8)	CM_L		I_F=10mA V_{CM}=1500V_{p-p} R_L=20kΩ	−10000	−15000	—	V / μs

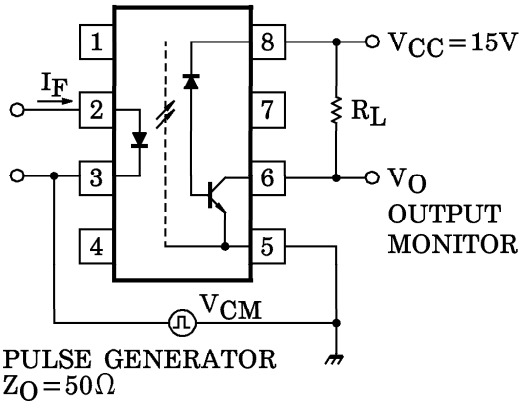
(Note 8) : CM_L is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state (V_O<1V).
CM_H is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state (V_O<4V).

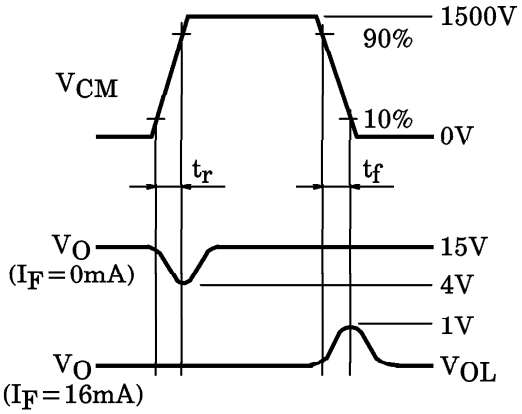
(Note 9) : Maximum electrostatic discharge voltage for any pins : 100V (C=200pF, R=0).

TEST CIRCUIT 1 : Switching time test circuit



TEST CIRCUIT 2 : Common mode noise immunity test circuit





$$CM_H=\frac{1200\ (V)}{t_r\ (\mu s)},\ CM_L=\frac{1200\ (V)}{t_f\ (\mu s)}$$