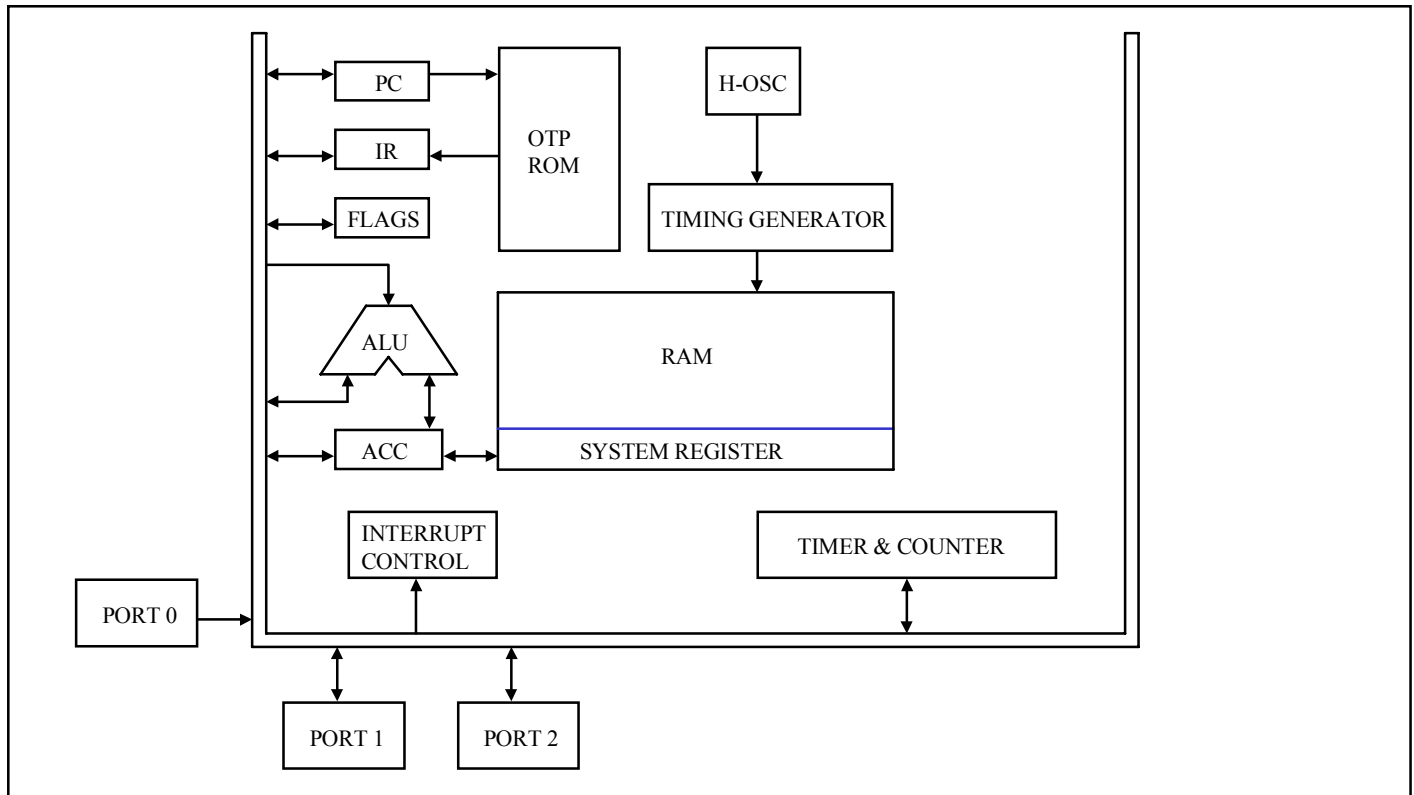


4. BLOCK DIAGRAM



5. PIN DESCRIPTION

<i>PIN NAME</i>	<i>TYPE</i>	<i>DESCRIPTION</i>
P1.0 ~ P1.3	I/O	Port 1.0 ~ Port 1.3 bi-direction pins.
RST/VPP	I	System reset inputs pin. Schmitt trigger structure, active "low", normal stay to "high". During program op-code, this pin be pull to 12.5Vdc to reset internal address counter and to write data into OTP-ROM.
VDD, VSS	P	Power supply input pins.
P2.0 ~ P2.7	I/O	Port 2.0 ~ Port 2.7 bi-direction pins.
XIN	I	Oscillator input pin.
XOUT	O	Oscillator output pin.
P0.0 / INT0	I	Port 0.0 and INT0 trigger pin with schmitt trigger structure.

Note : In order to reduce power consumption, the following procedure must be setup.

- a). In the SN8P0211/8P0111 version, P1.2 ~ P1.3 must be programmed to input mode with pull up resistor.

DISCLAIMER

The information appearing in SONiX web pages (“this publication”) is believed to be accurate. However, this publication could contain technical inaccuracies or typographical errors.

The reader should not assume that this publication is error-free or that it will be suitable for any particular purpose. SONiX makes no warranty, express, statutory implied or by description in this publication or other documents which are referenced by or linked to this publication. In no event shall SONiX be liable for any special, incidental, indirect or consequential damages of any kind, or any damages whatsoever, including, without limitation, those resulting from loss of use, data or profits, whether or not advised of the possibility of damage, and on any theory of liability, arising out of or in connection with the use or performance of this publication or other documents which are referenced by or linked to this publication.

This publication was developed for products offered in Taiwan. SONiX may not offer the products discussed in this document in other countries. Information is subject to change without notice. Please contact SONiX or its local representative for information on offerings available. Integrated circuits sold by SONiX are covered by the warranty and patent indemnification provisions stipulated in the terms of sale only.

The application circuits illustrated in this document are for reference purposes only. SONIX DISCLAIMS ALL WARRANTIES, INCLUDING THE WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PURPOSE. SONIX reserves the right to halt production or alter the specifications and prices, and discontinue marketing the Products listed at any time without notice. Accordingly, the reader is cautioned to verify that the data sheets and other information in this publication are current before placing orders.

Products described herein are intended for use in normal commercial applications.

Applications involving unusual environmental or reliability requirements, e.g. military equipment or medical life support equipment, are specifically not recommended without additional processing by SONIX for such application.