

KA5M0765RQC

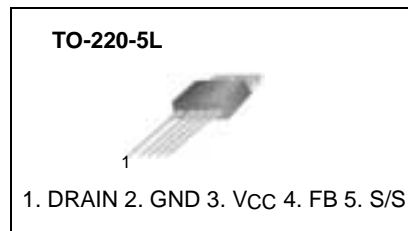
Fairchild Power Switch(FPS)

Features

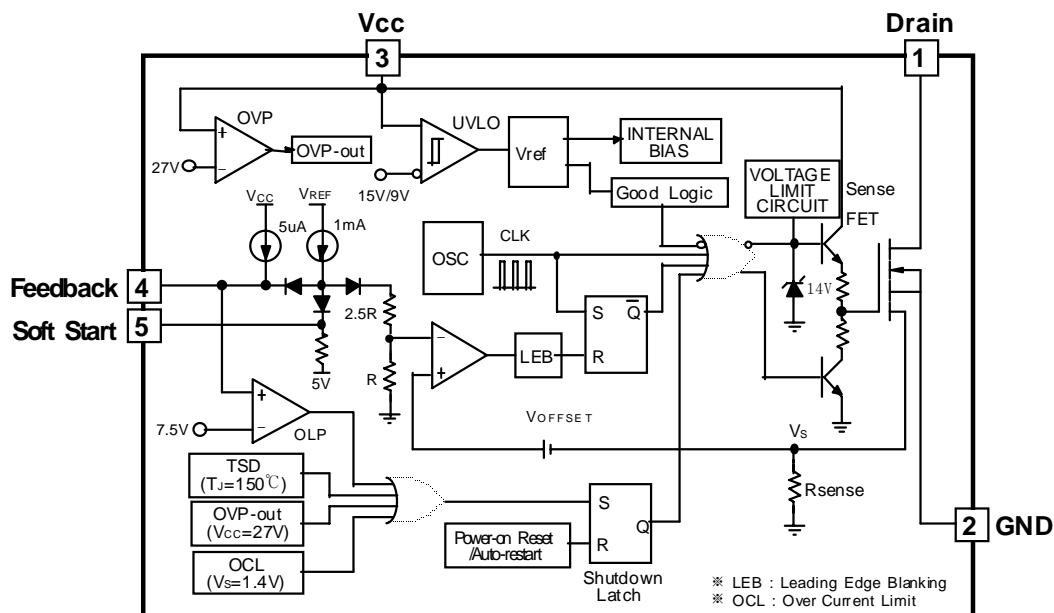
- Precision fixed operating frequency (70KHz)
- Low start-up current (Typ. 100µA)
- Pulse by pulse current limiting
- Over load protection
- Over current protection
- Over voltage protection (Min. 25V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

Description

The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed frequency oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shutdown protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry. compared to discrete MOSFET and PWM controller or RCC solution, a Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Maximum Drain voltage ⁽¹⁾	V _{D,MAX}	650	V
Drain-Gate voltage (R _{GS} =1MΩ)	V _{DGR}	650	V
Gate-source (GND) voltage	V _{GS}	±30	V
Drain current pulsed ⁽²⁾	I _{DM}	28.0	ADC
Single pulsed avalanche energy ⁽³⁾	E _{AS}	570	mJ
Continuous drain current (T _C =25°C)	I _D	7.0	ADC
Continuous drain current (T _C =100°C)	I _D	5.6	ADC
Maximum Supply voltage	V _{CC,MAX}	30	V
Input voltage range	V _{FB}	-0.3 to V _{SD}	V
Total power dissipation	P _D	140	W
	Derating	1.11	W/°C
Operating ambient temperature	T _A	-25 to +85	°C
Storage temperature	T _{STG}	-55 to +150	°C

Note:

1. T_j=25°C to 150°C
2. Repetitive rating: Pulse width limited by maximum junction temperature
3. L=24mH, starting T_j=25°C

Electrical Characteristics (SFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =50μA	650	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =Max., Rating, V _{GS} =0V	-	-	50	μA
		V _{DS} =0.8Max., Rating, V _{GS} =0V, T _C =125°C	-	-	200	μA
Static drain-source on resistance ^(note)	R _{DS(ON)}	V _{GS} =10V, I _D =3.5A	-	1.25	1.6	Ω
Forward transconductance ^(note)	g _{fs}	V _{DS} =50V, I _D =3.5A	3.0	-	-	S
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz	-	1120	-	pF
Output capacitance	C _{oss}		-	125	-	
Reverse transfer capacitance	C _{rss}		-	25	-	
Turn on delay time	t _{d(on)}	V _{DD} =0.5BV _{DSS} , I _D =7.0A (MOSFET switching time are essentially independent of operating temperature)	-	25	60	nS
Rise time	t _r		-	70	150	
Turn off delay time	t _{d(off)}		-	105	220	
Fall time	t _f		-	65	140	
Total gate charge (gate-source+gate-drain)	Q _g	V _{GS} =10V, I _D =7.0A, V _{DS} =0.8BV _{DSS}	-	38	50	nC
Gate-source charge	Q _{gs}		-	6.5	-	
Gate-drain (Miller) charge	Q _{gd}		-	18	-	

Note:

Pulse test: Pulse width ≤ 300μs, duty cycle ≤ 2%

$$S = \frac{1}{R}$$

Electrical Characteristics (Control part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start threshold voltage	VSTART	-	8.4	9	9.6	V
Stop threshold voltage	VSTOP	After turn on	14	15	16	V
OSCILLATOR SECTION						
Initial accuracy	FOSC	Ta=25°C	61	67	73	kHz
Frequency change with temperature ⁽²⁾	$\Delta F/\Delta T$	-25°C ≤ Ta ≤ +85°C	-	±5	±10	%
Maximum duty cycle	Dmax	-	74	77	80	%
FEEDBACK SECTION						
Feedback source current	IFB	Ta=25°C, 0V ≤ Vfb ≤ 3V	0.7	0.9	1.1	mA
Shutdown Feedback voltage	VSD	Vfb ≥ 6.5V	6.9	7.5	8.1	V
Shutdown delay current	Idelay	Ta=25°C, 5V ≤ Vfb ≤ VSD	4	5	6	μA
SOFT START SECTION						
Soft Start Voltage	VSS	VFB =2V	4.7	5.0	5.3	V
Soft Start Current	ISS	Sync & S/S=GND	0.8	1.0	1.2	mA
REFERENCE SECTION						
Output voltage ⁽¹⁾	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability ⁽¹⁾⁽²⁾	Vref/ΔT	-25°C ≤ Ta ≤ +85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT (SELF-PROTECTION) SECTION						
Peak Current Limit	IOVER	Max. inductor current	4.40	5.00	5.60	A
PROTECTION SECTION						
Thermal shutdown temperature (Tj) ⁽¹⁾	TSD	-	140	160	-	°C
TOTAL DEVICE SECTION						
Start Up current	I _{START}	VCC=14V	-	0.1	0.17	mA
Operating supply current (control part only)	IOP	VCC ≤ 28	-	7	12	mA

Note:

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process

Typical Performance Characteristics (SFET part)

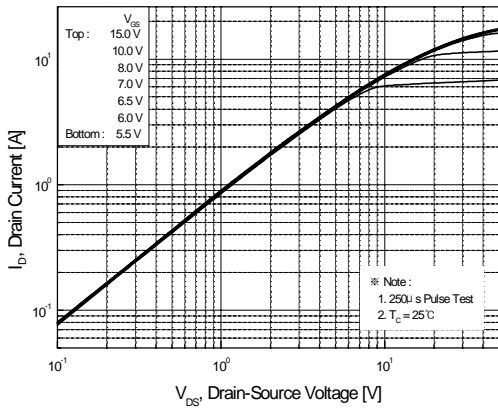


Figure 1. Output Characteristics

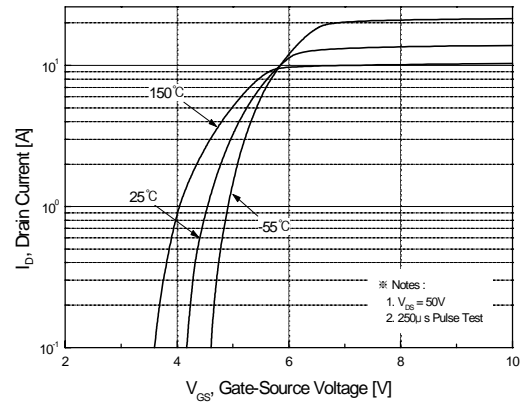


Figure 2. Transfer Characteristics

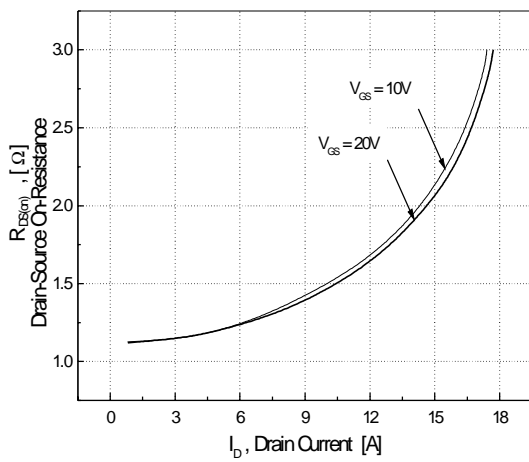


Figure 3. On-Resistance vs. Drain Current

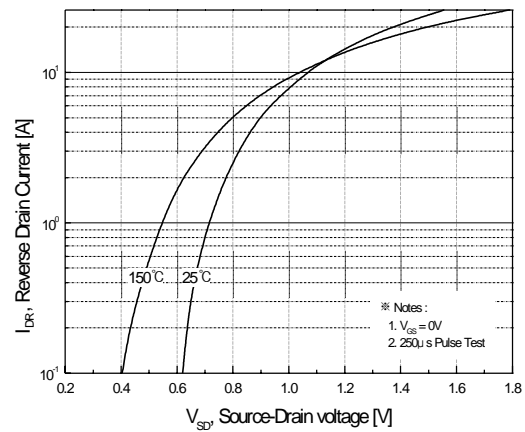


Figure 4. Source-Drain Diode Forward Voltage

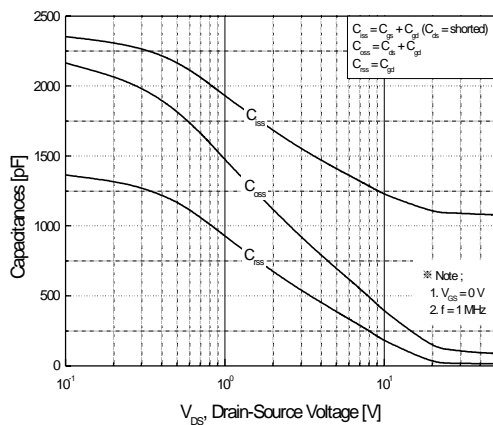


Figure 5. Capacitance vs. Drain-Source Voltage

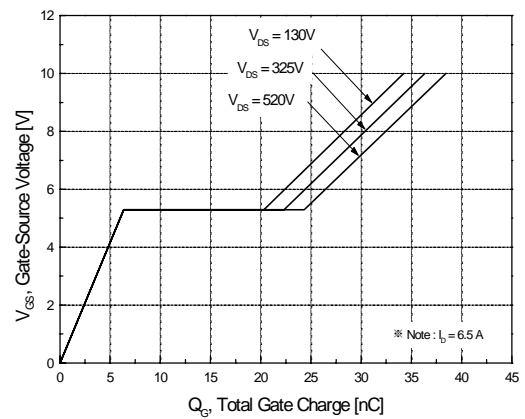


Figure 6. Gate Charge vs. Gate-Source Voltage

Typical Performance Characteristics (Continued)

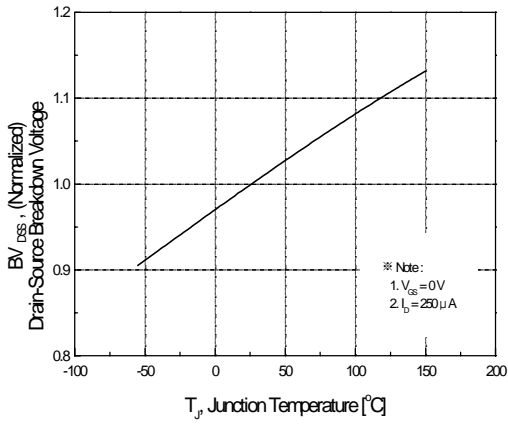


Figure 7. Breakdown Voltage vs. Temperature

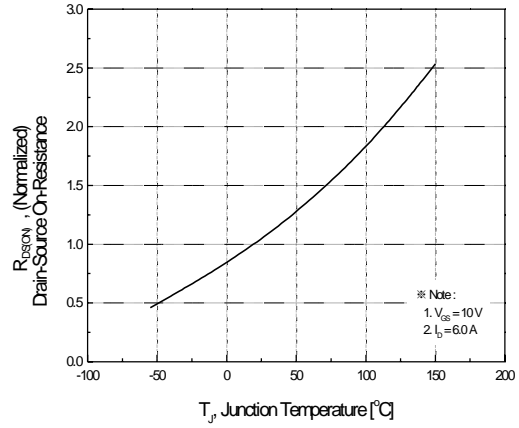


Figure 8. On-Resistance vs. Temperature

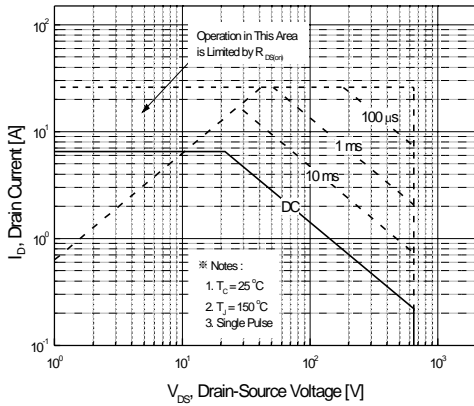


Figure 9. Max. Safe Operating Area

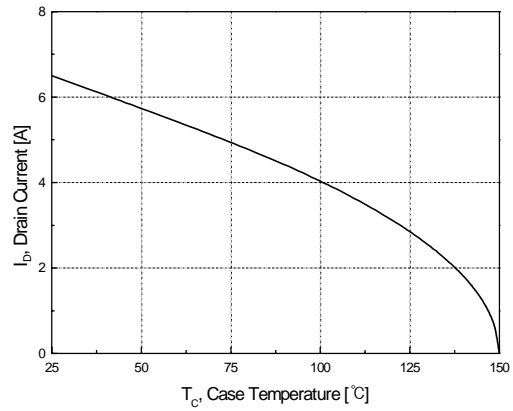


Figure 10. Max. Drain Current vs. Case Temperature

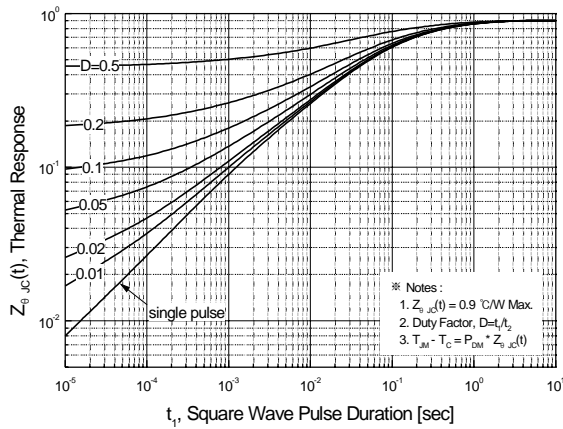


Figure 11. Thermal Response

Typical Performance Characteristics (Control part)

(These characteristic graphs are normalized at $T_a=25^\circ\text{C}$)

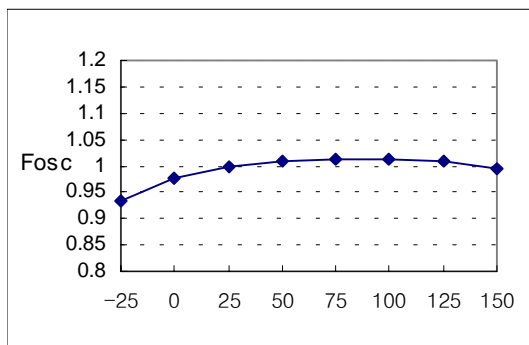


Figure 1. Operating Frequency

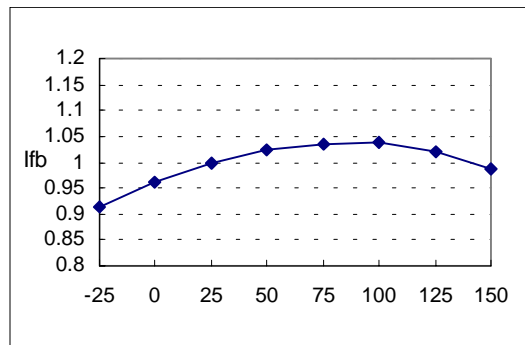


Figure 2. Feedback Source Current

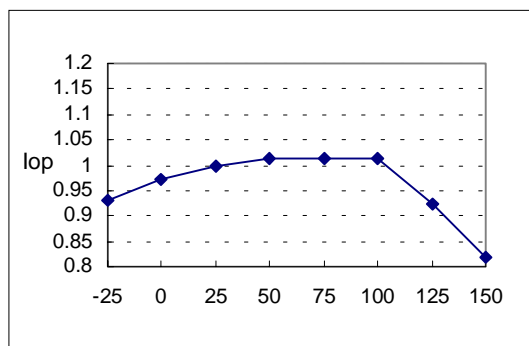


Figure 3. Operating Supply Current

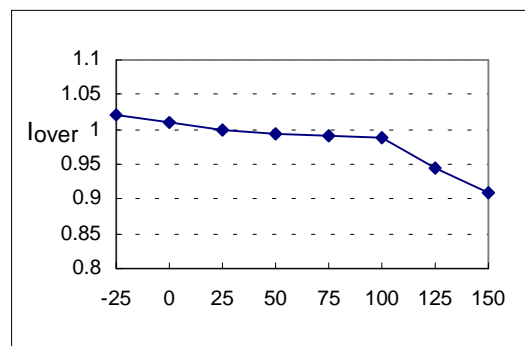


Figure 4. Peak Current Limit

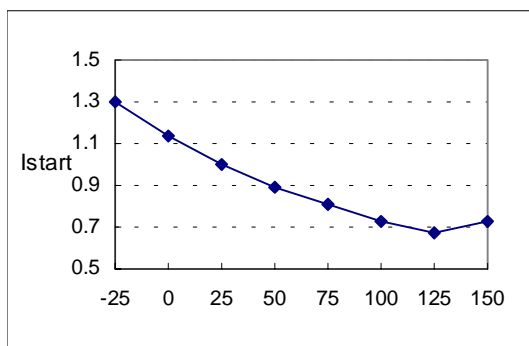


Figure 5. Start up Current

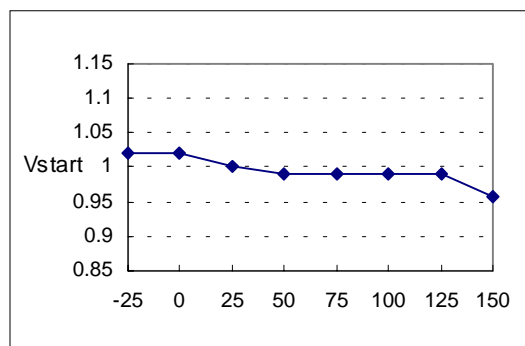


Figure 6. Start Thershold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

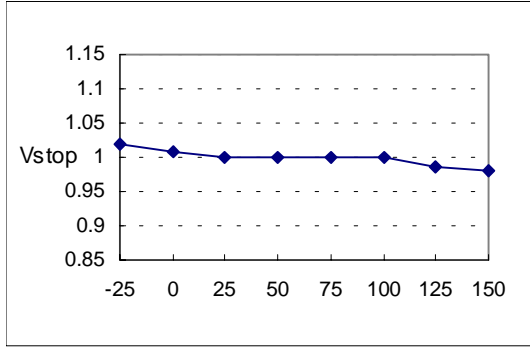


Figure 7. Stop Threshold Voltage

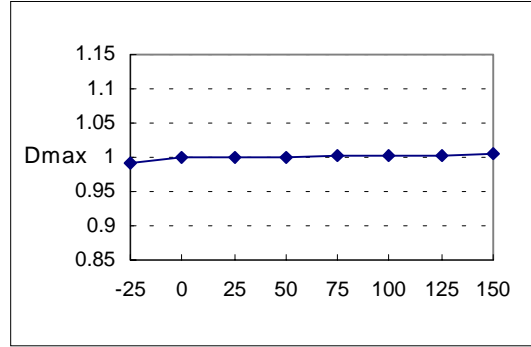


Figure 8. Maximum Duty Cycle

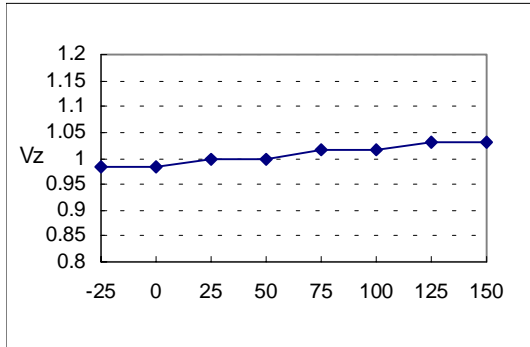


Figure 9. VCC Zener Voltage

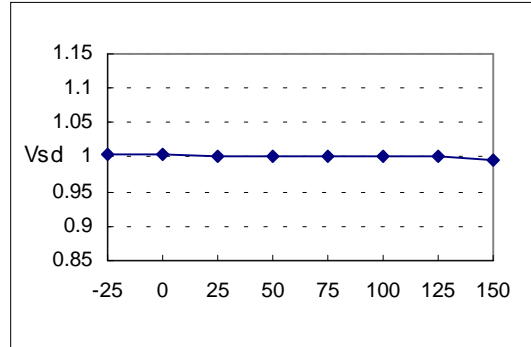


Figure 10. Shutdown Feedback Voltage

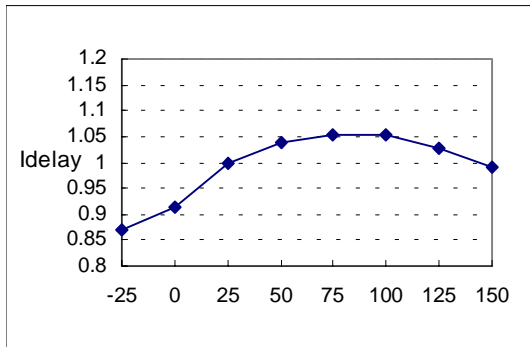


Figure 11. Shutdown Delay Current

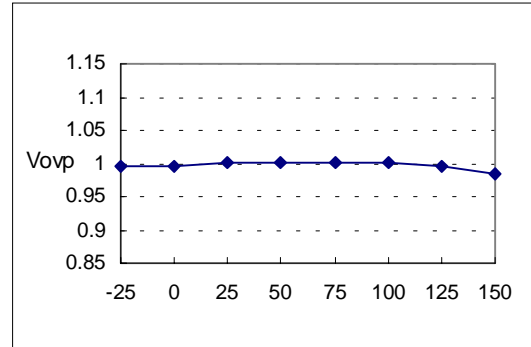


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at $T_a=25^\circ\text{C}$)

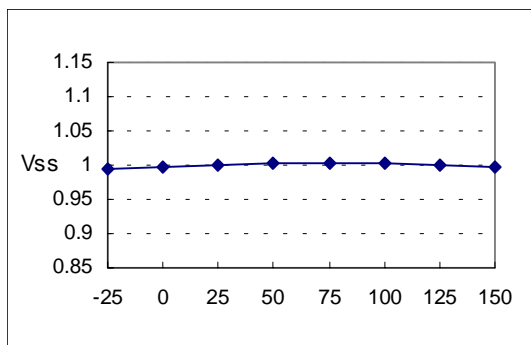


Figure 13. Soft Start Voltage

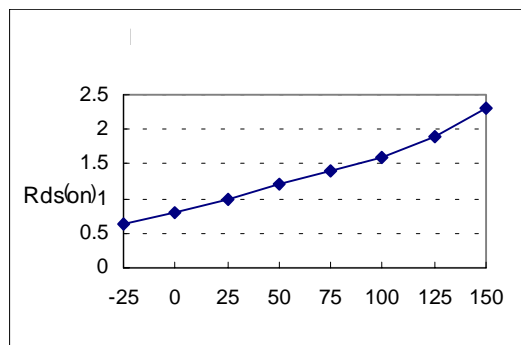
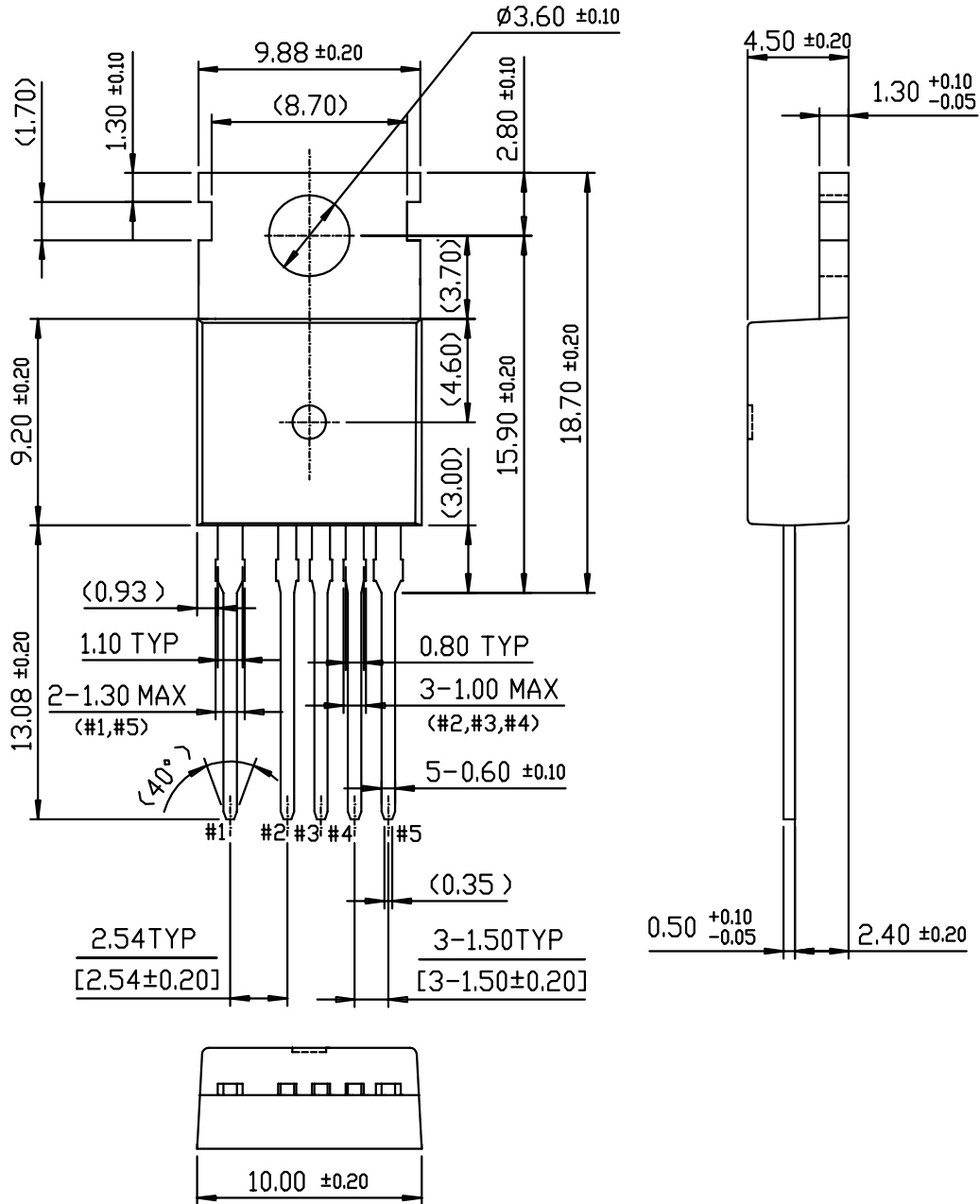


Figure 14. Static Drain-Source on Resistance

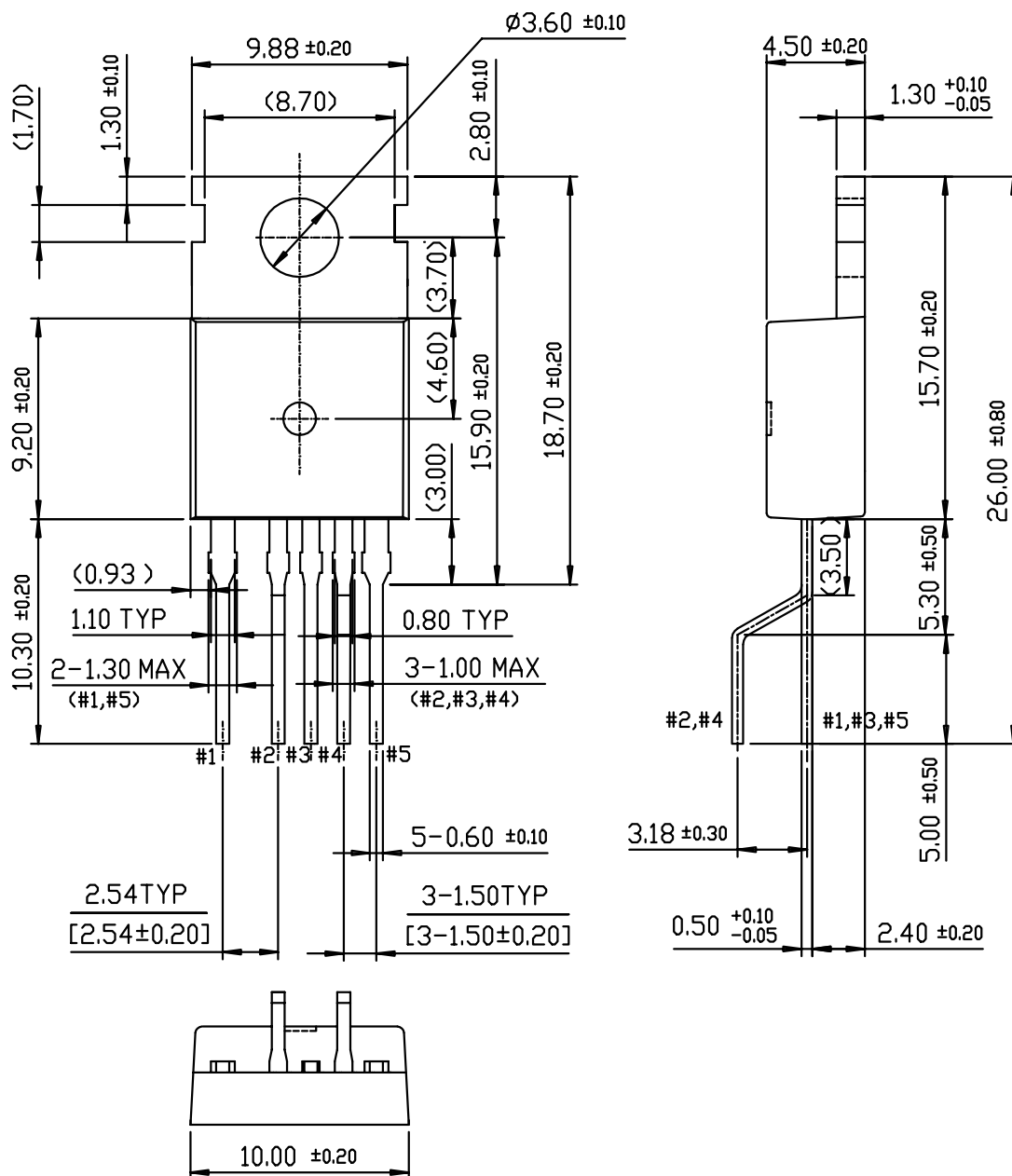
Package Dimensions

TO-220-5L



Package Dimensions

TO-220-5L(Forming)



Ordering Information

Product Number	Package	Rating	Topr (°C)
KA5M0765RQC-TU	TO-220F-5L	650V, 7A	-25°C to +85°C
KA5M0765RQC-YDTU	TO-220F-5L(Forming)		

TU : Non Forming Type

YDTU : Forming Type

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.